

AVTECH ELECTROSYSTEMS LTD.

NANOSECOND WAVEFORM ELECTRONICS
SINCE 1975

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BOX 5120, LCD MERIVALE
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INSTRUCTIONS

MODEL AV-1011B1-B

± 100 VOLT, 2 AMP, 100 kHz

2 ns RISE TIME

GENERAL PURPOSE LAB PULSE GENERATOR

AND LASER DIODE DRIVER

WITH IEEE 488.2 AND RS-232 CONTROL

SERIAL NUMBER: 14505

WARRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been disassembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.

TECHNICAL SUPPORT

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Manual Reference: /files/server1/officefiles/instructword/av-1011/AV-1011B1-B,sn14505.odt.
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INTRODUCTION

The AV-1011B1-B is a high performance, GPIB and RS232-equipped instrument capable of 200 W peak pulse power at repetition rates up to 100 kHz. The output amplitude is variable up to ± 100 V into 50 Ω . Pulse delay, advance and width are variable up to 1 ms. Rise times are fixed at less than 2 ns. The AV-1011B1-B provides single or double pulse output and can be triggered or gated by an external source. A front-panel pushbutton can also be used to trigger the instrument. The output pulse width can be set to follow an input trigger pulse width and the output amplitude can be controlled by an externally applied 0 to +10 Volts DC control voltage.

The AV-1011B1-B features front panel keyboard and adjust knob control of the output pulse parameters along with a four line by 40 character back-lit LCD display of the output amplitude, polarity, pulse width, pulse repetition frequency and delay. The instrument includes memory to store up to four complete instrument setups. The operator may use the front panel or the computer interface to store a complete “snapshot” of all key instrument settings, and recall this setup at a later time.

The output amplitude can also be controlled externally by applying a 0 to +10V DC control voltage to a rear panel BNC connector.

An internal power supply monitor removes the power to the output stage for five seconds if an average power overload exists. After that time, the unit operates normally for one second, and if the overload condition persists, the power is cut again. This cycle repeats until the overload is removed. The instrument will withstand duty cycles as high as 5%.

Application notes describing the use of the AV-1010 and AV-1011 families of pulse generators are available on the Avtech web site, <http://www.avtechpulse.com>.

This instrument is intended for use in research, development, test and calibration laboratories by qualified personnel.

SPECIFICATIONS

Model ¹ :	AV-1011B1-B
Amplitude ^{2,6} , with $Z_{OUT} = 2\Omega$: with $Z_{OUT} = 50\Omega$:	≤ 10 to 100 Volts ≤ 2 to 50 Volts
Pulse width, PW (FWHM) ³ :	100 ns to 1 ms
Rise time (20%-80%) ¹⁰ :	≤ 2 ns
Fall time (80%-20%) ¹⁰ :	≤ 2 ns for ≤ 100 μ s PW ≤ 8 ns for > 100 μ s PW
Pulse repetition frequency, PRF:	1 Hz to 100 kHz
Maximum duty cycle:	5%
Output impedance (Z_{OUT}) ⁵ :	$\approx 2\Omega$ or 50 Ω , switchable
Required load impedance:	50 Ω
Output polarity:	Positive or negative, switchable
Leading edge overshoot (into a non-inductive 50 Ω load):	$\leq 8\%$ of maximum rated amplitude for all amplitudes. Typically $< 3\%$ at maximum amplitude. Settles to $\pm 3\%$ of stable amplitude within 60 ns.
Typical pulse-top droop:	$\approx 5\%$ worst-case
Double pulse mode spacing:	1 μ s to 1 second (measured between the two leading edges of the pulse doublet). Must not exceed one-half of the period. There must be at least (PW + 100 ns) of "dead time" (no pulsing) between the trailing edge of the first pulse and the leading edge of the second pulse. For instance, if the pulse width is 1 μ s, the programmed delay between leading edges must be greater than 1 μ s (the pulse width) + 1.1 μ s (the minimum dead time) = 2.1 μ s, and the period must be greater than 4.2 μ s.
Sync output:	$> +3$ Volts, > 50 ns, will drive 50 Ohm loads
Gated operation:	TTL, synchronous or asynchronous, active high or low, switchable.
Trigger modes:	Internal trigger, external trigger (TTL-level pulse, > 10 ns, 1 k Ω input impedance), front-panel "Single Pulse" pushbutton, or single pulse trigger via computer command. In the external trigger mode, the pulse width may be set by the instrument, or it may be set to track the input pulse width.
Variable delay:	Sync to main output: 0 to ± 1.0 seconds, for all trigger modes (including external trigger).
Propagation delay:	≤ 150 ns (Ext trig in to pulse out)
Jitter:	$\leq \pm 35$ ps $\pm 0.015\%$ of sync delay (sync out to pulse out)
GPIO and RS-232 control ¹ :	Yes, standard feature on all -B units.
Ethernet port, for remote control using VXI-11.3, ssh, telnet, & web:	Included. Recommended as a modern alternative to GPIO / RS-232. See http://www.avtechpulse.com/options/vxi for details.
LabView drivers:	Available for download at http://www.avtechpulse.com/labview .
Settings resolution:	The resolution of the timing parameters (pulse width, delay, period) varies, but is always better than 0.15% of (set value + 20 ns). The amplitude resolution is $< 0.1\%$ of the maximum amplitude.
Settings accuracy:	Typically $\pm 3\%$ (plus ± 1 V or ± 2 ns) after 10 minute warmup, at low duty cycles ⁷ . For high-accuracy applications requiring traceable calibration, verify the output parameters with a calibrated oscilloscope ⁹ .
Output protection:	The output is protected against short circuits, open circuits, and high duty cycle
Connectors:	BNC female
Power requirements:	100 - 240 Volts, 50 - 60 Hz
Dimensions:	100 mm x 430 mm x 375 mm (3.9" x 17" x 14.8")
Chassis material, weight:	Anodized aluminum with blue-gray plastic trim. ≤ 10 kg / 22 lbs.
Temperature range:	+5°C to +40°C
Optional rack-mount kit:	Add the suffix "-R5" to the model number to include 19" rack mount kit

- 1) -B suffix indicates IEEE-488.2 GPIO and RS-232 control of amplitude and frequency. See <http://www.avtechpulse.com/gpio> for details.
- 2) The output amplitude may also be controlled by applying 0 to +10 V DC to a rear-panel BNC connector.
- 3) The output pulse width may also be controlled externally by applying a TTL-level trigger of the desired width to a rear-panel BNC connector (PWIN = PWOUT mode).
- 4) For adjustable (0 to ± 20 V) DC offset, add -OT to the model number (e.g., AV-1010-B-OT). When generating a pulse with positive amplitude, the offset plus amplitude must remain between 0 and +100V, and when generating a pulse with negative amplitude, the offset plus amplitude must remain between 0 and -100V.
- 5) This is the *internal* resistance *in series* with the output. It is *not* the load resistance.
- 6) The output can be set at lower values, but the overshoot may become significant relative to the pulse amplitude at low amplitudes.
- 7) The amplitude may decrease $\sim 10\%$ relative to the programmed setting if the instrument is operating at or near the maximum specified duty cycle.
- 8) Subject to the additional limitation that there must be at least 75 ns of "dead time" (no pulsing) between the trailing edge of one pulse and the leading edge of the next pulse.

- 9) These instruments are provided with a basic calibration checksheet, showing a selection of measured output parameters. These measurements are performed with equipment that is calibrated on a regular basis by a third-party ISO/IEC 17025:2005 accredited calibration laboratory. However, Avtech itself does not claim any accreditation. For applications requiring traceable performance, use a calibrated measurement system rather than relying on the accuracy of the pulse generator settings.
- 10) Valid into a 50 Ohm load.

REGULATORY NOTES

FCC PART 18

This device complies with part 18 of the FCC rules for non-consumer industrial, scientific and medical (ISM) equipment.

This instrument is enclosed in a rugged metal chassis and uses a filtered power entry module (where applicable). The main output signal is provided on a shielded connector that is intended to be used with shielded coaxial cabling and a shielded load. Under these conditions, the interference potential of this instrument is low.

If interference is observed, check that appropriate well-shielded cabling is used on the output connectors. Contact Avtech (info@avtechpulse.com) for advice if you are unsure of the most appropriate cabling. Also, check that your load is adequately shielded. It may be necessary to enclose the load in a metal enclosure.

If any of the connectors on the instrument are unused, they should be covered with shielded metal “dust caps” to reduce the interference potential.

This instrument does not normally require regular maintenance to minimize interference potential. However, if loose hardware or connectors are noted, they should be tightened. Contact Avtech (info@avtechpulse.com) if you require assistance.

EC DECLARATION OF CONFORMITY



We Avtech Electrosystems Ltd.
 P.O. Box 5120, LCD Merivale
 Ottawa, Ontario
 Canada K2C 3H5

declare that this pulse generator meets the intent of Directive 2014/30/EU for Electromagnetic Compatibility. Compliance pertains to the following specifications as listed in the official Journal of the European Communities:

EN 50081-1 Emission

EN 50082-1 Immunity

and that this pulse generator meets the intent of the Low Voltage Directive 2014/35/EU. Compliance pertains to the following specifications as listed in the official Journal of the European Communities:

EN 61010-1:2010+A1:2019, Safety requirements for electrical equipment for measurement, control, and laboratory use

DIRECTIVE 2011/65/EU (RoHS)

We Avtech Electrosystems Ltd.
P.O. Box 5120, LCD Merivale
Ottawa, Ontario
Canada K2C 3H5

declare that, to the best of our knowledge, all electrical and electronic equipment (EEE) sold by the company are in compliance with Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment (also known as “RoHS Recast”). In addition, this declaration of conformity is issued under the sole responsibility of Avtech Electrosystems Ltd. Specifically, products manufactured do not contain the substances listed in the table below in concentrations greater than the listed maximum value.

<i>Material/Substance</i>	<i>Threshold level</i>
Lead (Pb)	< 1000 ppm (0.1% by mass)
Mercury (Hg)	< 1000 ppm (0.1% by mass)
Hexavalent Chromium (Cr6+)	< 1000 ppm (0.1% by mass)
Polybrominated Biphenyls (PBB)	< 1000 ppm (0.1% by mass)
Polybrominated Diphenyl ethers (PBDE)	< 1000 ppm (0.1% by mass)
Cadmium (Cd)	< 100 ppm (0.01% by mass)
Bis(2-ethylhexyl) phthalate (DEHP)	< 1000 ppm (0.1% by mass)
Butyl benzyl phthalate (BBP)	< 1000 ppm (0.1% by mass)
Dibutyl phthalate (DBP)	< 1000 ppm (0.1% by mass)
Diisobutyl phthalate (DIBP)	< 1000 ppm (0.1% by mass)

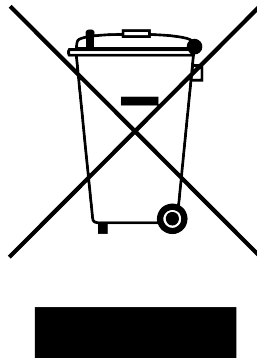
DIRECTIVE 2002/96/EC (WEEE)

European customers who have purchased this equipment directly from Avtech will have completed a “WEEE Responsibility Agreement” form, accepting responsibility for WEEE

compliance (as mandated in Directive 2002/96/EC of the European Union and local laws) on behalf of the customer, as provided for under Article 9 of Directive 2002/96/EC.

Customers who have purchased Avtech equipment through local representatives should consult with the representative to determine who has responsibility for WEEE compliance. Normally, such responsibilities will lie with the representative, unless other arrangements (under Article 9) have been made.

Requirements for WEEE compliance may include registration of products with local governments, reporting of recycling activities to local governments, and financing of recycling activities.



FIRMWARE LICENSING

This instrument uses open-source software internally, and is built on a Fedora Linux operating system.

Some of this software requires that the source code be made available to the user as a condition of its licensing. The source code for programs used by Fedora is freely available at <https://src.fedoraproject.org/>.

The source code for modifications implemented by Avtech, and for Avtech-specific programs, is available at <https://www.avtechpulse.com/cgit/Instrument.git/>.

INSTALLATION

VISUAL CHECK

After unpacking the instrument, examine to ensure that it has not been damaged in shipment. Visually inspect all connectors, knobs, liquid crystal displays (LCDs), and the handles. Confirm that a power cord, a GPIB cable, and two instrumentation manuals (this manual and the “Programming Manual for -B Instruments”) are with the instrument. If the instrument has been damaged, file a claim immediately with the company that transported the instrument.

POWER RATINGS

This instrument is intended to operate from 100 - 240 V, 50 - 60 Hz.

The maximum power consumption is 90 Watts. Please see the “FUSES” section for information about the appropriate AC and DC fuses.

This instrument is an “Installation Category II” instrument, intended for operation from a normal single-phase supply.

CONNECTION TO THE POWER SUPPLY

An IEC-320 three-pronged recessed male socket is provided on the back panel for AC power connection to the instrument. One end of the detachable power cord that is supplied with the instrument plugs into this socket. The other end of the detachable power cord plugs into the local mains supply. Use only the cable supplied with the instrument. The mains supply must be earthed, and the cord used to connect the instrument to the mains supply must provide an earth connection. (The supplied cord does this.)

 Warning: Failure to use a grounded outlet may result in injury or death due to electric shock. This product uses a power cord with a ground connection. It must be connected to a properly grounded outlet. The instrument chassis is connected to the ground wire in the power cord.

The table below describes the power cord that is normally supplied with this instrument, depending on the destination region:

Destination Region	Description	Option	Manufacturer	Part Number
United Kingdom, Hong Kong, Singapore, Malaysia	BS 1363, 230V, 50 Hz	-AC00	Qualtek	370001-E01
Australia, New Zealand	AS 3112:2000, 230-240V, 50 Hz	-AC01	Qualtek	374003-A01
Continental Europe, Korea, Indonesia, Russia	European CEE 7/7 "Schuko" 230V, 50 Hz	-AC02	Qualtek	364002-D01
North America, Taiwan	NEMA 5-15, 120V, 60 Hz	-AC03	Qualtek	312007-01
Switzerland	SEV 1011, 230V, 50 Hz	-AC06	Qualtek	378001-E01
South Africa, India	SABS 164-1, 220-250V, 50 Hz	-AC17	Volex	2131H 10 C3
Japan	JIS 8303, 100V, 50-60 Hz	-AC18	Qualtek	397002-01
Israel	SI 32, 220V, 50 Hz	-AC19	Qualtek	398001-01
China	GB 1002-1 / 2099-1, 220V, 50 Hz	-AC22	Qualtek	399012-01

PROTECTION FROM ELECTRIC SHOCK

 Operators of this instrument must be protected from electric shock at all times. The owner must ensure that operators are prevented access and/or are insulated from every connection point. In some cases, connections must be exposed to potential human contact. Operators must be trained to protect themselves from the risk of electric shock. This instrument is intended for use by qualified personnel who recognize shock hazards and are familiar with safety precautions required to avoid possibly injury. In particular, operators should:

1. Keep exposed high-voltage wiring to an absolute minimum.
2. Wherever possible, use shielded connectors and cabling.
3. Connect and disconnect loads and cables only when the instrument is turned off.
4. Keep in mind that all cables, connectors, oscilloscope probes, and loads must have an appropriate voltage rating.
5. Do not attempt any repairs on the instrument, beyond the fuse replacement procedures described in this manual. Contact Avtech technical support (see page 2 for contact information) if the instrument requires servicing. Service is to be performed solely by qualified service personnel.

ENVIRONMENTAL CONDITIONS

This instrument is intended for use under the following conditions:

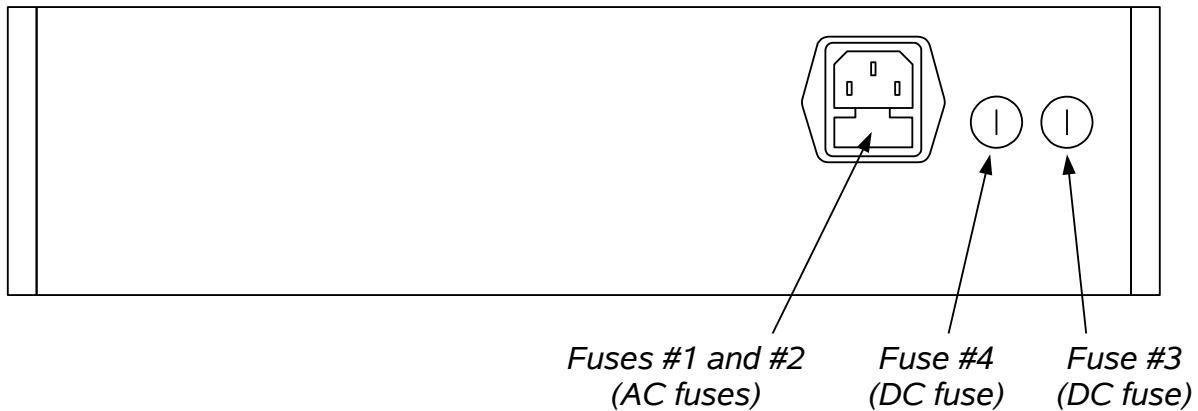
1. indoor use;
2. altitude up to 2 000 m;
3. temperature 5 °C to 40 °C;
4. maximum relative humidity 80 % for temperatures up to 31 °C decreasing linearly to 50 % relative humidity at 40 °C;
5. Mains supply voltage fluctuations up to ± 10 % of the nominal voltage;
6. no pollution or only dry, non-conductive pollution.

LABVIEW DRIVERS

A LabVIEW driver for this instrument is available for download on the Avtech web site, at <http://www.avtechpulse.com/labview>. A copy is also available in National Instruments' Instrument Driver Library at <http://www.natinst.com/>.

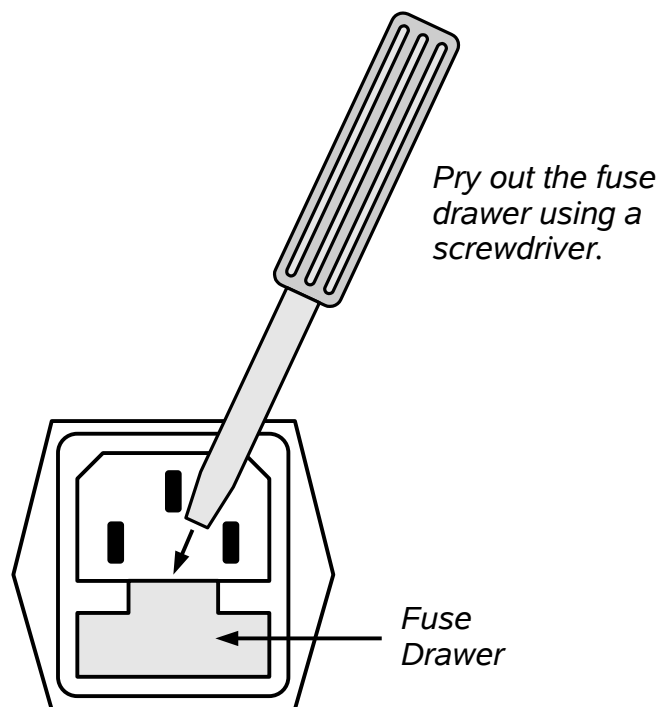
FUSES

This instrument contains four fuses. All are accessible from the rear-panel. Two protect the AC prime power input, and two protect the internal DC power supplies. The locations of the fuses on the rear panel are shown in the figure below:



AC FUSE REPLACEMENT

To physically access the AC fuses, the power cord must be detached from the rear panel of the instrument. The fuse drawer may then be extracted using a small flat-head screwdriver, as shown below:



DC FUSE REPLACEMENT

The DC fuses may be replaced by inserting the tip of a flat-head screwdriver into the fuse holder slot, and rotating the slot counter-clockwise. The fuse and its carrier will then pop out.

FUSE RATINGS

The following table lists the required fuses:

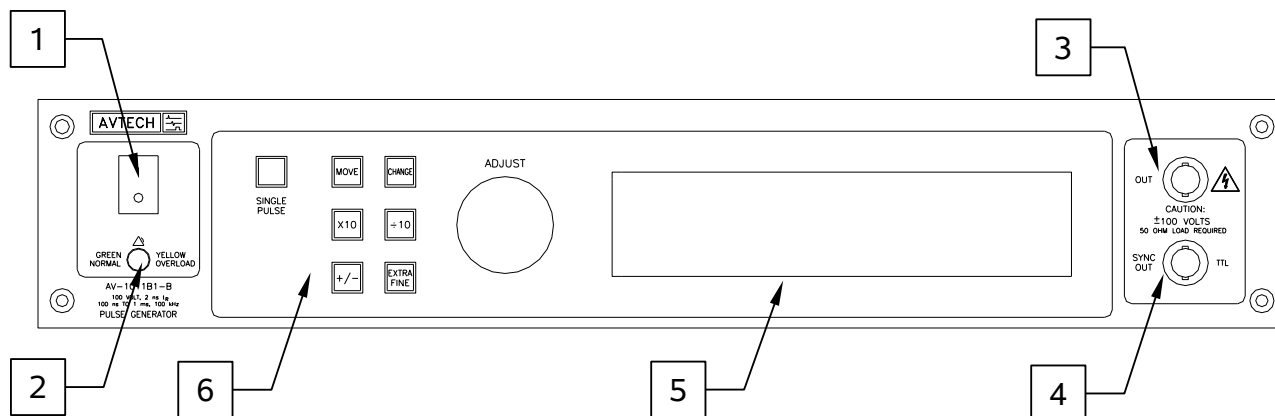
Fuses	Nominal Mains Voltage	Rating	Case Size	Recommended Replacement Part	
				Littelfuse Part Number	Digi-Key Stock Number
#1, #2 (AC)	115 V	0.8A, 250V, Time-Delay	5×20 mm	0218.800HXP	F2418-ND
	230 V	0.5A, 250V, Time-Delay	5×20 mm	0218.500HXP	F2416-ND
#3 (DC)	N/A	1.6A, 250V, Time-Delay	5×20 mm	021801.6HXP	F2424-ND
#4 (DC)	N/A	1.6A, 250V, Time-Delay	5×20 mm	021801.6HXP	F2424-ND

Certain combinations of features may require slightly different fuse ratings. The required fuse ratings will be clearly marked on the rear panel of the instrument.

The recommended fuse manufacturer is Littelfuse (<http://www.littelfuse.com>).

Replacement fuses may be easily obtained from Digi-Key (<http://www.digikey.com>) and other distributors.

FRONT PANEL CONTROLS



1. POWER Switch. This is the main power switch. When turning the instrument on, there is normally a delay of 10 seconds before anything is shown on the main display, as the internal operating system boots up.
2. OVERLOAD Indicator. When the instrument is powered, this indicator is normally green, indicating normal operation. If this indicator is yellow, an internal automatic overload protection circuit has been tripped. If the unit is overloaded (by operating at an exceedingly high duty cycle or by operating into a very low impedance), the protective circuit will disable the output of the instrument and turn the indicator light yellow. The light will stay yellow (i.e. output disabled) for about 5 seconds after which the instrument will attempt to re-enable the output (i.e. light green) for about 1 second. If the overload condition persists, the output will be disabled again (i.e. light yellow) for another 5 seconds. If the overload condition has been removed, the instrument will resume normal operation.

This overload indicator may flash yellow briefly at start-up. This is not a cause for concern.

Note that the output stage will safely withstand a short-circuited load condition.

3. OUT CONNECTOR. This BNC connector provides the main output signal, into load impedances of 50Ω. (The instrument will not function properly, and may even be damaged, if operated into other impedances.)

 **Caution:** Voltages as high as $\pm 100\text{V}$ may be present on the center conductor of this output connector. Avoid touching this conductor. Connect to this connector using standard coaxial cable, to ensure that the center conductor is not exposed.

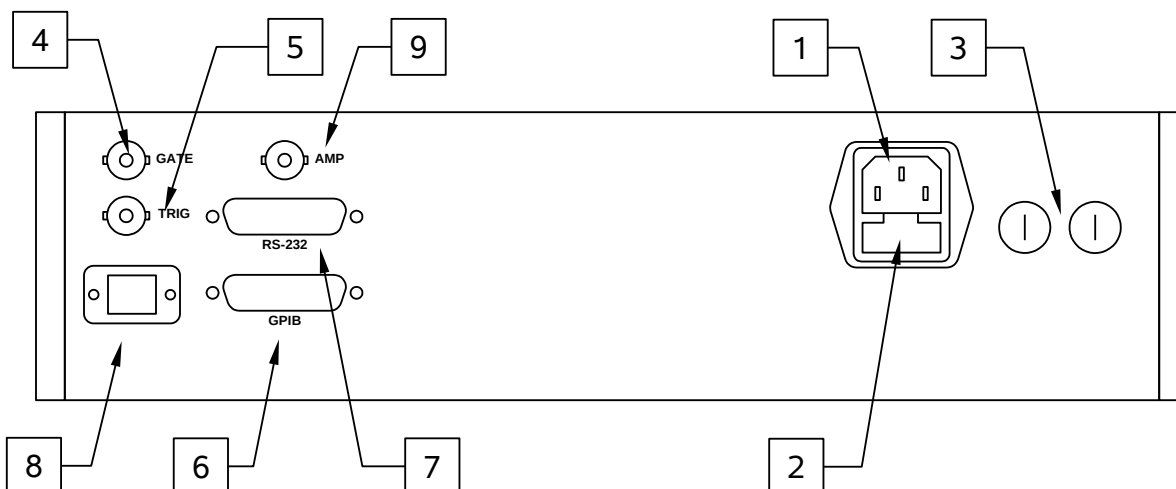
4. SYNC OUT. This connector supplies a SYNC output that can be used to trigger other equipment, particularly oscilloscopes. This signal leads (or lags) the main output by a duration set by the "DELAY" controls and has an approximate amplitude of +3 Volts to $R_L > 50\Omega$ with a pulse width of approximately 100 ns.

5. LIQUID CRYSTAL DISPLAY (LCD). This LCD is used in conjunction with the keypad to change the instrument settings. Normally, the main menu is displayed, which lists the key adjustable parameters and their current values. The “Programming Manual for -B Instruments” describes the menus and submenus in detail.

6. KEYPAD.

Control Name	Function
MOVE	This moves the arrow pointer on the display.
CHANGE	This is used to enter the submenu, or to select the operating mode, pointed to by the arrow pointer.
×10	If one of the adjustable numeric parameters is displayed, this increases the setting by a factor of ten.
÷10	If one of the adjustable numeric parameters is displayed, this decreases the setting by a factor of ten.
+/-	If one of the adjustable numeric parameters is displayed, and this parameter can be both positive or negative, this changes the sign of the parameter.
EXTRA FINE	This changes the step size of the ADJUST knob. In the extra-fine mode, the step size is twenty times finer than in the normal mode. This button switches between the two step sizes.
ADJUST	This large knob adjusts the value of any displayed numeric adjustable values, such as frequency, pulse width, etc. The adjust step size is set by the "EXTRA FINE" button. When the main menu is displayed, this knob can be used to move the arrow pointer.

REAR PANEL CONTROLS



Note: some connectors may be in different positions than shown above, depending on the exact combination of options ordered.

1. AC POWER INPUT. An IEC-320 C14 three-pronged recessed male socket is provided on the back panel for AC power connection to the instrument. One end of the detachable power cord that is supplied with the instrument plugs into this socket.
2. AC FUSE DRAWER. The two fuses that protect the AC input are located in this drawer. Please see the “FUSES” section of this manual for more information.
3. DC FUSES. These two fuses protect the internal DC power supplies. Please see the “FUSES” sections of this manual for more information.
4. GATE. This TTL-level (0 and +5V) logic input can be used to gate the triggering of the instrument. This input can be either active high or active low, depending on the front panel settings or programming commands. (The instrument triggers normally when this input is unconnected). When set to active high mode, this input is pulled-down to ground by a 1 k Ω resistor. When set to active low mode, this input is pulled-up to +5V by a 1 k Ω resistor.
5. TRIG. This TTL-level (0 and +5V) logic input can be used to trigger the instrument, if the instrument is set to triggering externally. The instrument triggers on the rising edge of this input. The input impedance of this input is 1 k Ω . (Depending on the length of cable attached to this input, and the source driving it, it may be desirable to add a coaxial 50 Ohm terminator to this input to provide a proper transmission line termination. The Pasternack (www.pasternack.com) PE6008-50 BNC feed-thru 50 Ohm terminator is suggested for this purpose.)

When triggering externally, the instrument can be set such that the output pulse width tracks the pulse width on this input, or the output pulse width can be set independently.

6. GPIO Connector. A standard GPIO cable can be attached to this connector to allow the instrument to be computer-controlled. See the “Programming Manual for -B Instruments” for more details on GPIO control.
7. RS-232 Connector. A standard serial cable with a 25-pin male connector can be attached to this connector to allow the instrument to be computer-controlled. A user name (“admin”) and a password (“default”, as shipped from the factory) are required when logging into a serial terminal session. The internal controller attempts to auto-sense the parity setting. It may be necessary to send a few return characters before attempting a login in order to provide enough data to allow this auto-sensing to work. (A standard Linux “agetty” process is used to implement serial control internally.) See the “Programming Manual for -B Instruments” for more details on RS-232 control.
8. Network Connector. This Ethernet connector allows the instrument to be remotely controlled using the VXI-11.3, ssh (secure shell), telnet, and http (web) protocols. See the “Programming Manual for -B Instruments” for more details.
9. AMP Connector. The output amplitude can be set to track the voltage on this input. Zero Volts in corresponds to zero amplitude output, and +10V in corresponds to maximum amplitude out. This mode is activated by selecting "Ext Control" on the front-panel amplitude menu, or with the "source:voltage external" command.

GENERAL INFORMATION

BASIC PULSE CONTROL

This instrument can be triggered by its own internal clock or by an external TTL trigger signal. In either case, two output channels respond to the trigger: OUT and SYNC. The OUT channel is the signal that is applied to the load. Its amplitude and pulse width are variable. The SYNC pulse is a fixed-width TTL-level reference pulse used to trigger oscilloscopes or other measurement systems. When the delay is set to a positive value the SYNC pulse precedes the OUT pulse. When the delay is set to a negative value the SYNC pulse follows the OUT pulse.

These pulses are illustrated below, assuming internal triggering and a positive delay:

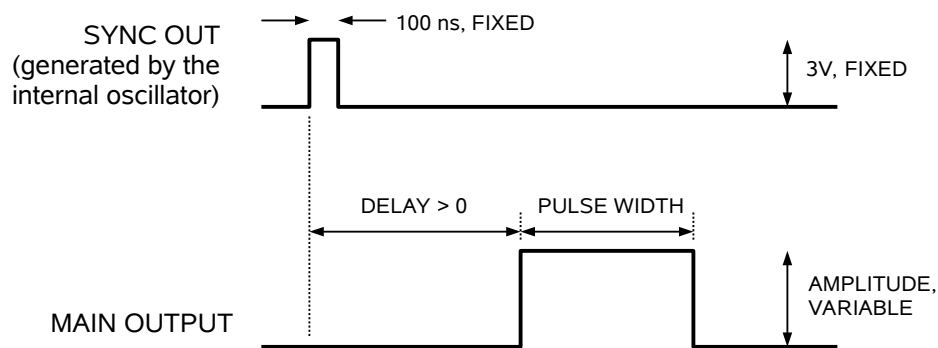


Figure A

If the delay is negative, the order of the SYNC and OUT pulses is reversed:

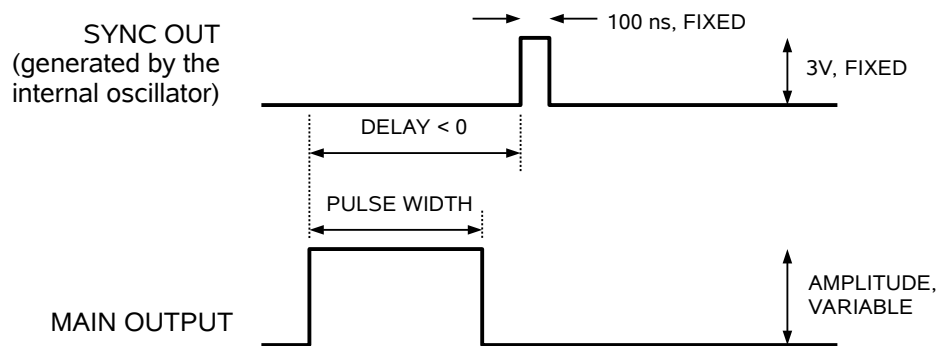


Figure B

The next figure illustrates the relationship between the signal when an external TTL-level trigger is used:

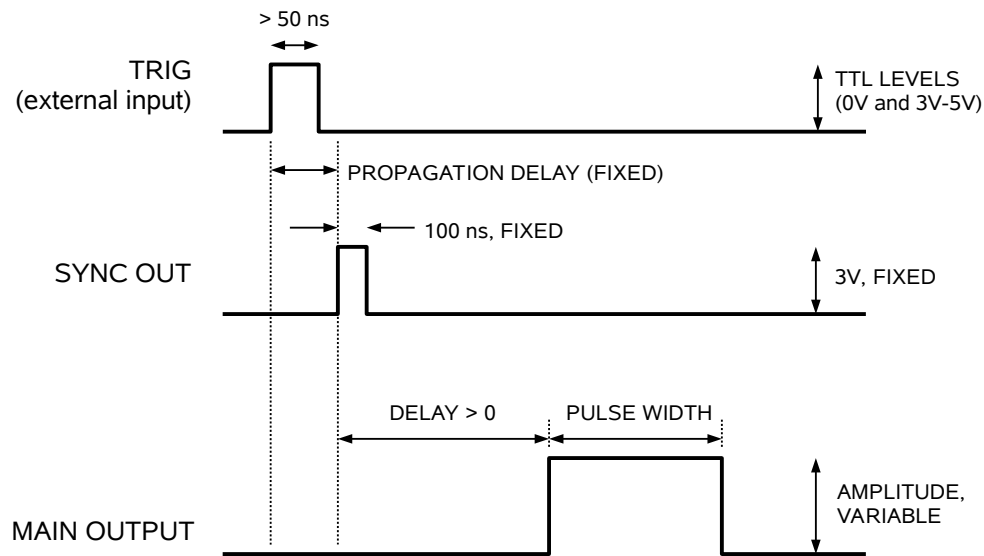


Figure C

As before, if the delay is negative, the order of the SYNC and OUT pulses is reversed.

The last figure illustrates the relationship between the signal when an external TTL-level trigger is used in the $PW_{\text{IN}} = PW_{\text{OUT}}$ mode. In this case, the output pulse width equals the external trigger's pulse width (approximately), and the delay circuit is bypassed:

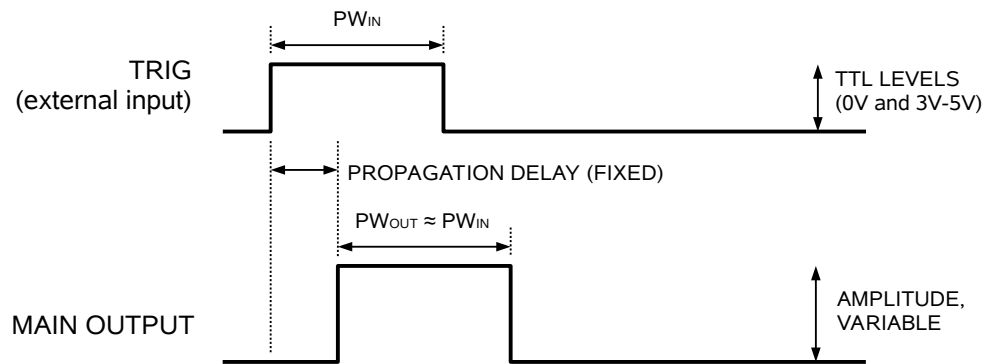


Figure D

The delay, pulse width, and frequency (when in the internal mode), of the OUT pulse can be varied with front panel controls or via the GPIB or RS-232 computer interfaces.

TRIGGER MODES

This instrument has four trigger modes:

- Internal Trigger: the instrument controls the trigger frequency, and generates the clock internally.
- External Trigger: the instrument is triggered by an external TTL-level clock on the back-panel TRIG connector.
- Manual Trigger: the instrument is triggered by the front-panel “SINGLE PULSE” pushbutton.
- Hold Trigger: the instrument is set to not trigger at all.

These modes can be selected using the front panel trigger menu, or by using the appropriate programming commands. (See the “Programming Manual for -B Instruments” for more details.)

PULSE WIDTH MODES

This instrument has two pulse width modes:

- Normal: the instrument controls the output pulse width.
- $PW_{IN}=PW_{OUT}$: the output pulse width equals the pulse width of the trigger signal on the “TRIG” connector. The instrument must be in the external trigger mode.

These modes can be selected using the front panel pulse width menu, or by using the appropriate programming commands. (See the “Programming Manual for -B Instruments” for more details.)

GATING MODES

Triggering can be suppressed by a TTL-level signal on the rear-panel GATE connector. The instrument can be set to stop triggering when this input high or low, using the front-panel gate menu or the appropriate programming commands. This input can also be set to act synchronously or asynchronously. When set to asynchronous mode, the GATE will disable the output immediately. Output pulses may be truncated. When set to synchronous mode, the output will complete the full pulse width if the output is high, and then stop triggering. No pulses are truncated in this mode.

MINIMIZING WAVEFORM DISTORTIONS

OUTPUT IMPEDANCE

The output impedance is the resistance in series with the output, inside the instrument. It is different than the load impedance.

The output impedance of the AV-1011B1-B can be set at 2 Ω or 50 Ω using the front panel controls (or GPIB/RS-232 commands).

When operating with 50 Ω output impedance and 50 Ω load impedance, the output impedance and the load impedance will cause a resistive voltage-divider effect, reducing the output amplitude at the load by a factor of two. Thus, the 50 Ω output impedance can only be used to generate amplitudes of 0 to $\pm 50V$.

When the output impedance is set to 50 Ω , internal attenuators are switched in at lower amplitudes to provide a cleaner output waveform. For this reason, the output impedance should be set to 50 Ω when output amplitudes of $\sim 25V$ or less are desired.

LOAD IMPEDANCE

The AV-1011B1-B requires a 50 Ω load impedance. If the load you intend to drive has a higher impedance, add a resistance in parallel with the load so that the total effective resistance is 50 Ω .

Two methods are available for driving load impedances lower than 50 Ω , such as laser diodes. The first method consists of adding a resistance in series with the diode such that the sum of the two impedances is 50 Ω . In this approach, the maximum obtainable output current is 2 Amps (i.e., 100V / 50 Ω).

The second method is similar, except that a transformer (Avtech model AVX-MRB5 or AVX-MRB6) is added between the pulse generator and the load impedance. This allows lower impedances to be driven with higher currents.

For more information on using the AV-1010 and AV-1011 series of pulse generators to drive laser diodes, see the application note “No. 3A ~ Choosing & Using Pulsed Constant-Current Sources, and Blue Diode Considerations” at <http://www.avtechpulse.com/appnote>.

USE 50 Ω TRANSMISSION LINES

Connect the load to the pulse generator with 50 Ω transmission lines (e.g. RG-58 or RG-174 cable).

USE LOW-INDUCTANCE LOADS

Lenz's Law predicts that for an inductive voltage spike will be generated when the current through an inductance changes. Specifically, $V_{\text{SPIKE}} = L \times dI_{\text{LOAD}}/dt$, where L is the inductance, I_{LOAD} is the load current change, and t is time. For this reason, it is important to keep any parasitic in the load low. This means keeping wiring short, and using low inductance components. In particular, wirewound resistors should be avoided.

The Ohmite (www.ohmite.com) "OY" series of 2 Watt resistors are an example of low-inductance resistors suitable for pulse applications.

OSCILLOSCOPE SELECTION AND BANDWIDTH ISSUES

To observe a signal with a rise time of t_R , the bandwidth of the oscilloscope must exceed $1/t_R$. For the AV-1011B1-B, the rise time is typically 1 ns, so $1/t_R = 1$ GHz. For best results, the bandwidth of the oscilloscope (and any attenuators and cables used in the measurement system) should exceed 2 GHz.

Most oscilloscopes with suitable bandwidth are "sampling" oscilloscopes, which are more specialized in nature than slower conventional "real-time" oscilloscopes. Most sampling oscilloscopes have a very limited input voltage range ($\pm 1\text{V}$, for example). It is essential in these cases to use high-bandwidth attenuators on the input of the oscilloscope, to avoid over-driving or damaging the oscilloscope.

If a high-bandwidth sampling oscilloscope is not available, a slower real-time oscilloscope can be used, but the rising and falling edges will not be accurately displayed. Significant overshoot and ringing will be observed as a result of the oscilloscope's limited bandwidth. (Contrary to some user's expectations, low-bandwidth oscilloscopes *do not* "round off" the signal like a classic low-pass filter.) These are measurement artifacts, and are not normally present on the actual output.

OPERATIONAL CHECK

This section describes a sequence to confirm the basic operation of the instrument. It should be performed after receiving the instrument. It is a useful learning exercise as well.

Before proceeding with this procedure, finish reading this instruction manual thoroughly. Then read the “Local Control” section of the “OP1B Interface Programming Manual” thoroughly. The “Local Control” section describes the front panel controls used in this operational check - in particular, the MOVE, CHANGE, and ADJUST controls.

1. Connect a cable from the SYNC OUT connector to the TRIG input of an oscilloscope. Connect a 10W (or higher) 50 Ω load to the OUT connector and place the scope probe across this load. Set the oscilloscope to trigger externally with the vertical setting at 5 Volts/div and the horizontal setting at 1 us/div.
2. Turn on the AV-1011B1-B. The main menu will appear on the LCD.
3. To set the AV-1011B1-B to trigger from the internal clock at a PRF of 10 kHz:
 - a) The arrow pointer should be pointing at the frequency menu item. If it is not, press the MOVE button until it is.
 - b) Press the CHANGE button. The frequency submenu will appear. Rotate the ADJUST knob until the frequency is set at 10 kHz.
 - c) The arrow pointer should be pointing at the “Internal” choice. If it is not, press MOVE until it is.
 - d) Press CHANGE to return to the main menu.
4. To set the delay to 1 us:
 - a) Press the MOVE button until the arrow pointer is pointing at the delay menu item.
 - b) Press the CHANGE button. The delay submenu will appear. Rotate the ADJUST knob until the delay is set at 1 us.
 - c) The arrow pointer should be pointing at the “Normal” choice. If it is not, press MOVE until it is.
 - d) Press CHANGE to return to the main menu.
5. To set the pulse width to 1 us:

- a) Press the MOVE button until the arrow pointer is pointing at the pulse width menu item.
 - b) Press the CHANGE button. The pulse width submenu will appear. Rotate the ADJUST knob until the pulse width is set at 1 us.
 - c) The arrow pointer should be pointing at the “Normal” choice. If it is not, press MOVE until it is.
 - d) Press CHANGE to return to the main menu.
6. At this point, nothing should appear on the oscilloscope.
7. To enable the output:
 - a) Press the MOVE button until the arrow pointer is pointing at the output menu item.
 - b) Press the CHANGE button. The output submenu will appear.
 - c) Press MOVE until the arrow pointer is pointing at the “ON” choice.
 - d) Press CHANGE to return to the main menu.
8. To change the output amplitude:
 - a) Press the MOVE button until the arrow pointer is pointing at the amplitude menu item.
 - b) Press the CHANGE button. The amplitude submenu will appear. Rotate the ADJUST knob until the amplitude is set at 100V.
 - c) Observe the oscilloscope. You should see 1 us wide, 100V pulses.
 - d) Rotate the ADJUST knob. The amplitude as seen on the oscilloscope should vary. Set it at 100V.
 - e) Press CHANGE to return to the main menu.
9. Try varying the pulse width, by repeating step (5). As you rotate the ADJUST knob, the pulse width on the oscilloscope will change. It should agree with the displayed value. Stay below 5% duty cycle.
10. To change the output amplitude polarity:
 - a) Press the MOVE button until the arrow pointer is pointing at the amplitude menu item.

- b) Press the CHANGE button. The amplitude submenu will appear.
- c) Press the “+/-” button. The amplitude as seen on the oscilloscope should become negative.
- d) Press CHANGE to return to the main menu.

This completes the operational check.

PROGRAMMING YOUR PULSE GENERATOR

KEY PROGRAMMING COMMANDS

The “OP1B Interface Programming Manual” describes in detail how to connect the pulse generator to your computer, and the programming commands themselves. A large number of commands are available; however, normally you will only need a few of these. Here is a basic sample sequence of commands that might be sent to the instrument after power-up:

*rst	(resets the instrument)
trigger:source internal	(selects internal triggering)
frequency 1000 Hz	(sets the frequency to 1000 Hz)
pulse:width 10 us	(sets the pulse width to 10 us)
pulse:delay 1 us	(sets the delay to 1 us)
output:impedance 2	(sets the output impedance to 2Ω)
volt 50	(sets the amplitude to 50 V)
output on	(turns on the output)

For triggering a single event, this sequence would be more appropriate:

*rst	(resets the instrument)
trigger:source hold	(turns off all triggering)
pulse:width 10 us	(sets the pulse width to 10 us)
output:impedance 50	(sets the output impedance to 50Ω)
output on	(turns on the output)
volt 50	(sets the amplitude to 50 V)
trigger:source immediate	(generates a single non-repetitive trigger event)
trigger:source hold	(turns off all triggering)
output off	(turns off the output)

To set the instrument to trigger from an external TTL signal applied to the rear-panel TRIG connector, use:

*rst	(resets the instrument)
trigger:source external	(selects external triggering)
pulse:width 10 us	(sets the pulse width to 10 us)
pulse:delay 1 us	(sets the delay to 1 us)
output:impedance 2	(sets the output impedance to 2Ω)
volt 50	(sets the amplitude to 50 V)
output on	(turns on the output)

In the above example, the pulse width of the output was set by a programming command. To set the output pulse width to track the trigger pulse width in external mode, use:

*rst	(resets the instrument)
trigger:source external	(selects internal triggering)
pulse:width in	($PW_{OUT} = PW_{IN}$)
output:impedance 2	(sets the output impedance to 2Ω)
volt 50	(sets the amplitude to 50 V)
output on	(turns on the output)

These commands will satisfy 90% of your programming needs.

ALL PROGRAMMING COMMANDS

For more advanced programmers, a complete list of the available commands is given below. These commands are described in detail in the “Programming Manual for -B Instruments”. (Note: this manual also includes some commands that are not implemented in this instrument. They can be ignored.)

<u>Keyword</u>	<u>Parameter</u>	<u>Notes</u>
OUTPut:		
:LOAD	50 10000	
:[STATE]	<boolean value>	
:IMPedance	<numeric value>	
:PROTection		
:TRIPped?		[query only]
[SOURce]:		
:FREQuency		
[:CW FIXed]	<numeric value>	
[SOURce]:		
:PULSe		
:PERiod	<numeric value>	
:WIDTh	<numeric value> IN	
:DCYClE	<numeric value>	
:HOLD	WIDTh DCYClE	
:DELay	<numeric value>	
:DOUBle		
[:STATE]	<boolean value>	
:DELay	<numeric value>	
:GATE		
:TYPE	ASYNc SYNc	
:LEVel	HIgh LOw	
[SOURce]:		
:VOLTage		
[:LEVel]		
[:IMMediate]		
[:AMPLitude]	<numeric value> EXTeRnal	
:LOW	<numeric value>	[for -OT option only]
:PROTection		

:TRIPped?		[query only]
STATUS:		
:OPERation		
:[EVENT]?		[query only, always returns "0"]
:CONDition?		[query only, always returns "0"]
:ENABLE	<numeric value>	[implemented but not useful]
:QUESTionable		
:[EVENT]?		[query only, always returns "0"]
:CONDition?		[query only, always returns "0"]
:ENABLE	<numeric value>	[implemented but not useful]
SYSTem:		
:COMMunicate		
:GPIB		
:ADDRes	<numeric value>	
:SERial		
:CONTrol		
:RTS	ON IBFull RFR	
:[RECeive]		
:BAUD	1200 2400 4800 9600 19200 38400 57600 115200	
:ERRor		
:[NEXT]?		[query only]
:COUNT?		[query only]
:VERSion?		[query only]
TRIGger:		
:SOURce	INTernal EXTernal MANual HOLD IMMEDIATE	
*CLS		[no query form]
*ESE	<numeric value>	
*ESR?		[query only]
*IDN?		[query only]
*OPC		
*SAV	0 1 2 3	[no query form]
*RCL	0 1 2 3	[no query form]
*RST		[no query form]
*SRE	<numeric value>	
*STB?		[query only]
*TST?		[query only]
*WAI		[no query form]

OTHER INFORMATION

APPLICATION NOTES

Application notes are available on the Avtech web site, at <http://www.avtechpulse.com/appnote>.

MANUAL FEEDBACK

Please report any errors or omissions in this manual, or suggestions for improvement, to info@avtechpulse.com. Thanks!

MECHANICAL INFORMATION

TOP COVER REMOVAL

If necessary, the interior of the instrument may be accessed by removing the four Phillips screws on the top panel. With the four screws removed, the top cover may be slid back (and off).

 Always disconnect the power cord and allow the instrument to sit unpowered for 10 minutes before opening the instrument. This will allow any internal stored charge to discharge.

There are no user-adjustable internal circuits. For repairs other than fuse replacement, please contact Avtech (info@avtechpulse.com) to arrange for the instrument to be returned to the factory for repair. Service is to be performed solely by qualified service personnel.

 Caution: High voltages are present inside the instrument during normal operation. Do not operate the instrument with the cover removed.

RACK MOUNTING

A rack mounting kit is available. The -R5 rack mount kit may be installed after first removing the one Phillips screw on the side panel adjacent to the front handle.

ELECTROMAGNETIC INTERFERENCE

To prevent electromagnetic interference with other equipment, all used outputs should be connected to shielded loads using shielded coaxial cables. Unused outputs should be terminated with shielded coaxial terminators or with shielded coaxial dust caps, to prevent unintentional electromagnetic radiation. All cords and cables should be less than 3m in length.

MAINTENANCE

REGULAR MAINTENANCE

This instrument does not require any regular maintenance.

On occasion, one or more of the four rear-panel fuses may require replacement. All fuses can be accessed from the rear panel. See the “FUSES” section for details.

CLEANING

If desired, the interior of the instrument may be cleaned using compressed air to dislodge any accumulated dust. (See the “TOP COVER REMOVAL” section for instructions on accessing the interior.) No other cleaning is recommended.

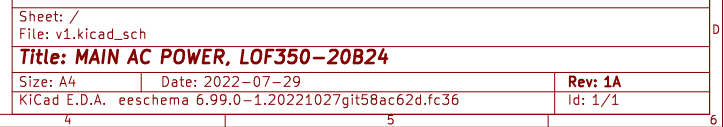
TRIGGER DAMAGE

The rear-panel TRIG input, used in the external trigger mode, is protected by a diode clamping circuit. However, the protection circuit is not foolproof, and it is possible for a grossly excessive signal to damage the trigger circuitry on the main timing control board (the 4×10 inch board on the right side of the instrument).

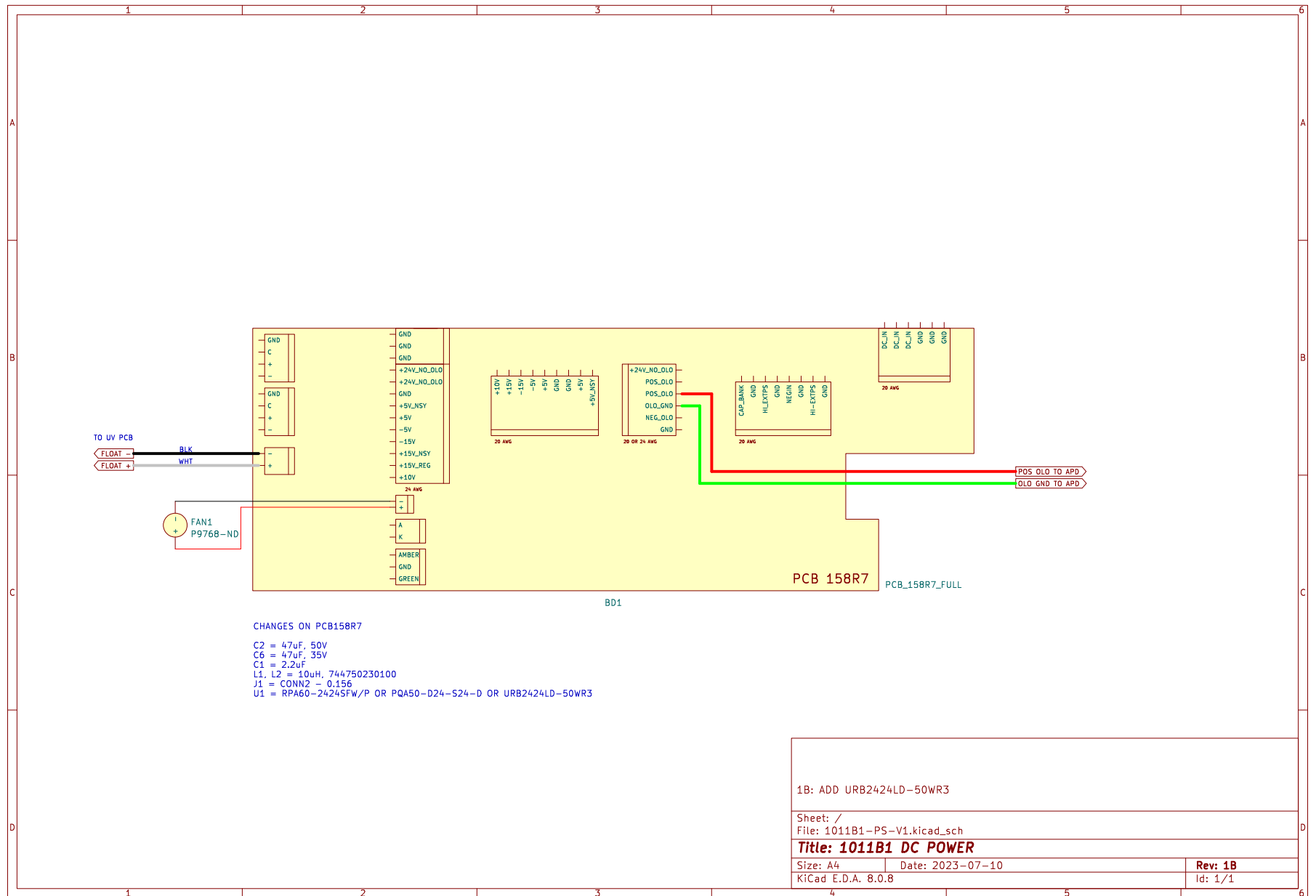
The IC that is most likely to fail under these conditions is installed in a socket. It is a standard TTL IC in a 16-pin plastic DIP package, model 74F151 or equivalent.

If you suspect that this IC has been damaged, turn off the power and replace this IC. It may be replaced by a 74F151, 74LS151, 74ALS151, or 74HCT151.

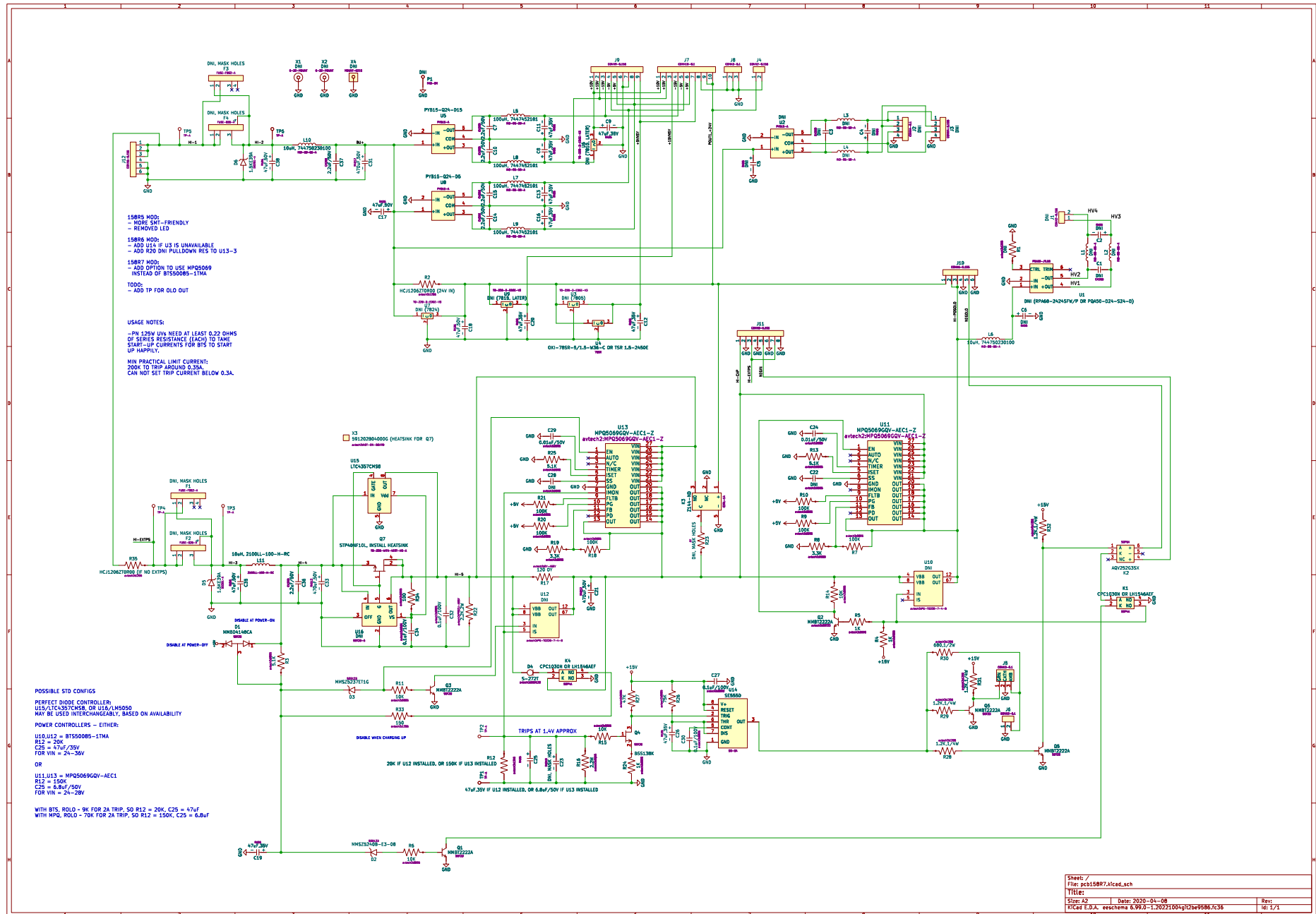
WIRING OF AC POWER



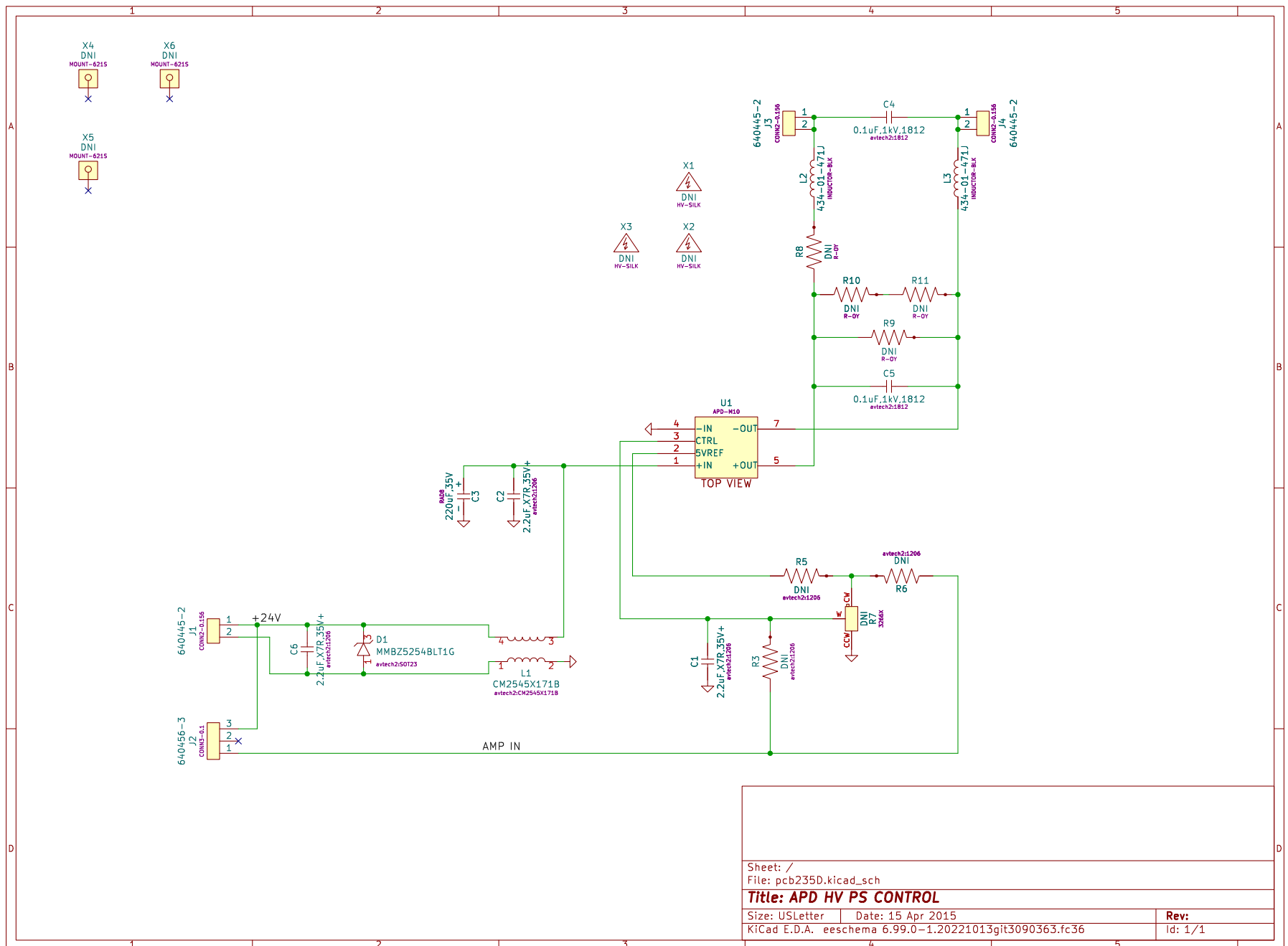
WIRING OF FLOATING DC POWER



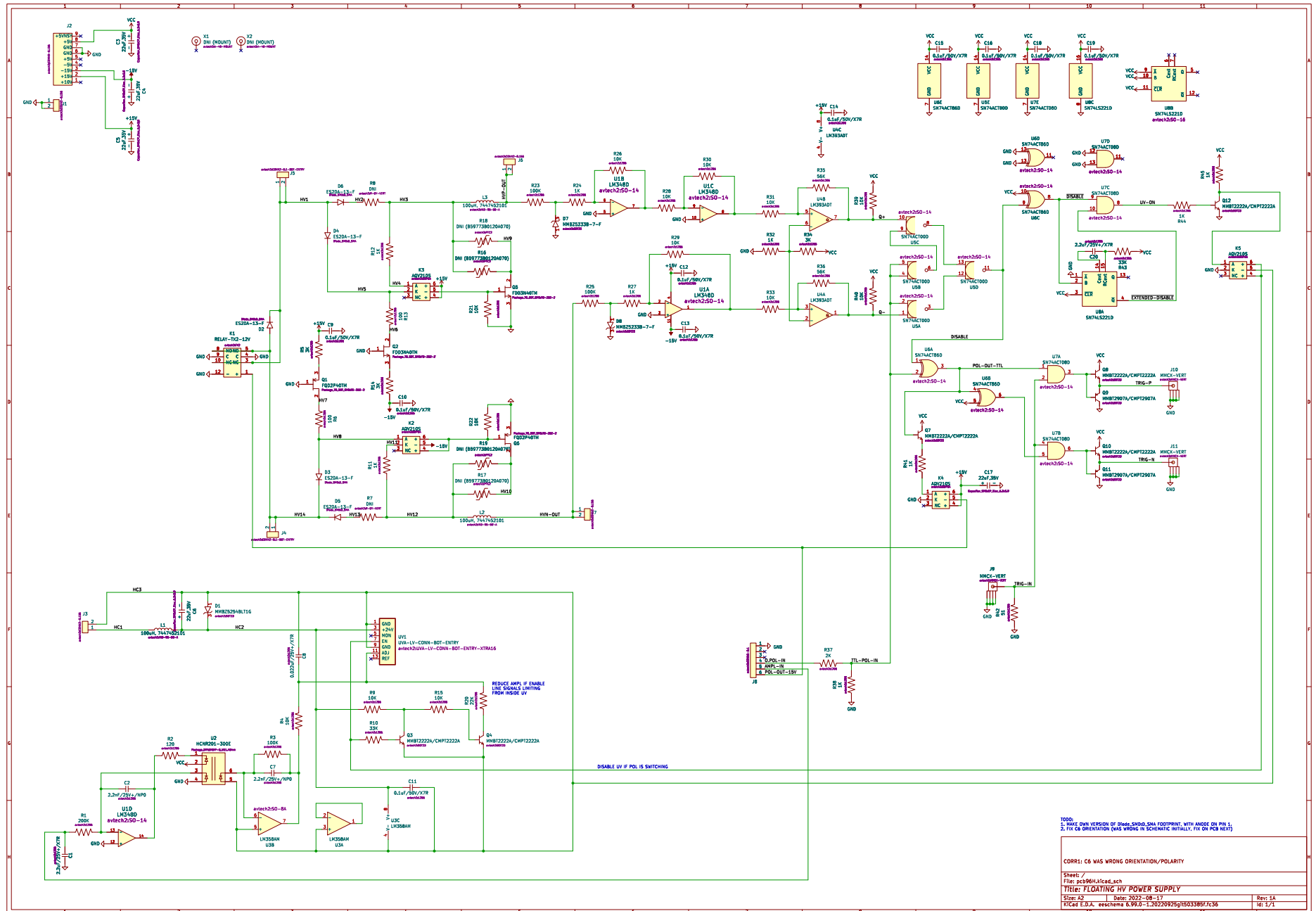
PCB 158R7 - LOW VOLTAGE POWER SUPPLY



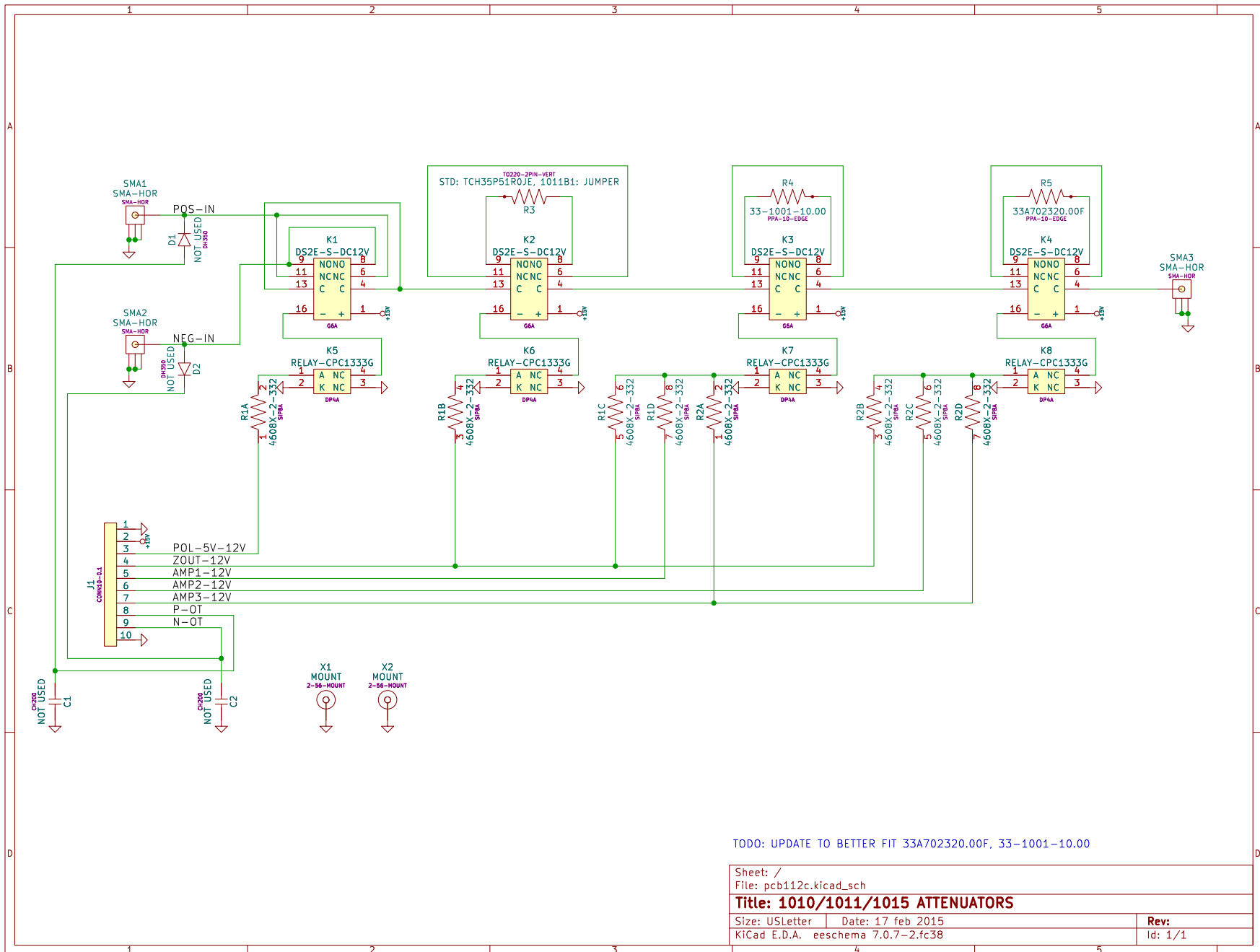
PCB 235D - HIGH VOLTAGE DC POWER SUPPLY



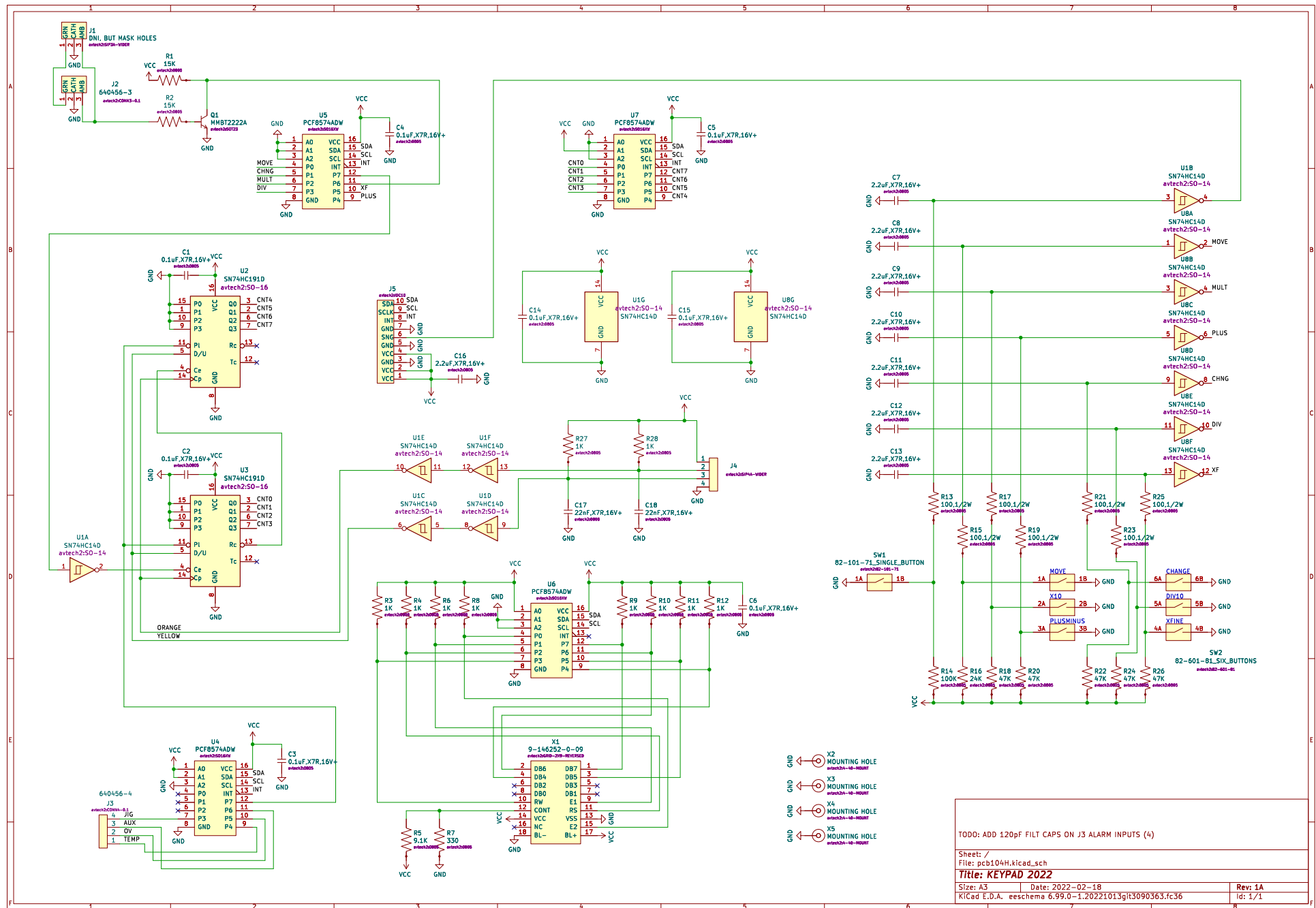
PCB 96H - HIGH VOLTAGE DC POWER SUPPLY



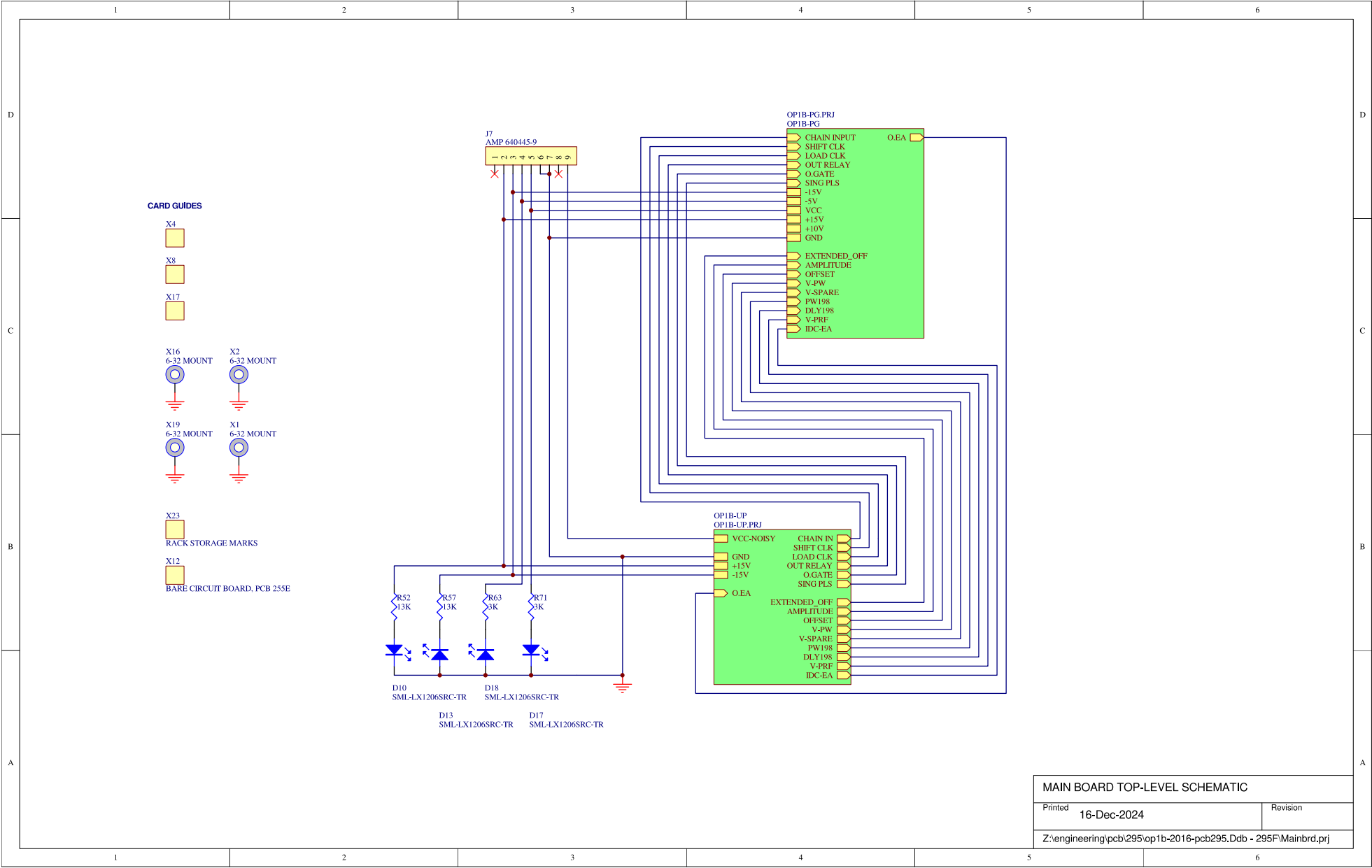
PCB 112C - AMPLITUDE RANGE CONTROL



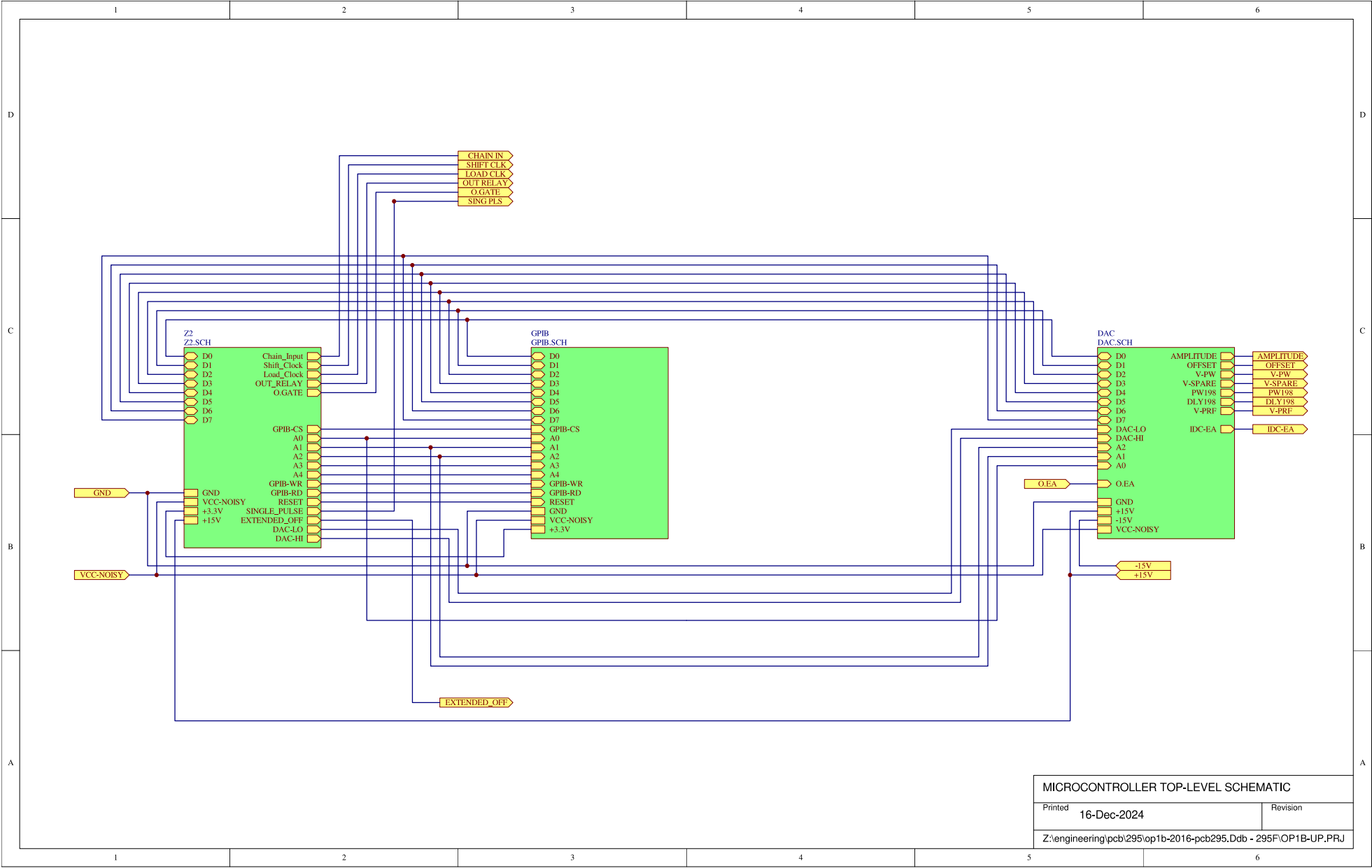
PCB 104H - KEYPAD / DISPLAY BOARD



PCB 295F - MAIN CONTROL BOARD, 1 OF 13, TOP-LEVEL



PCB 295F - MAIN CONTROL BOARD, 2 OF 13, MICROCONTROLLER TOP-LEVEL



MICROCONTROLLER TOP-LEVEL SCHEMATIC

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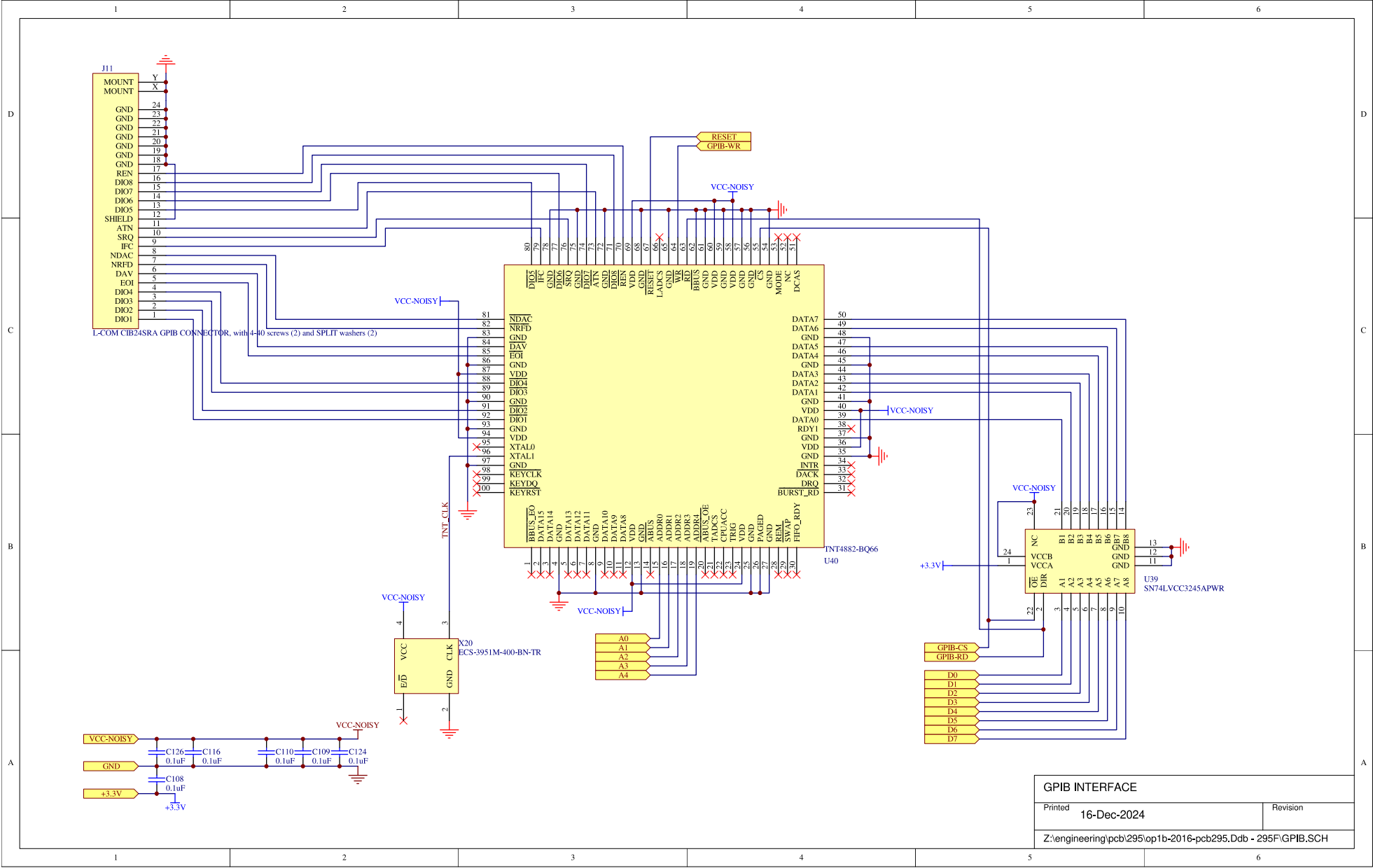
The image shows a complex PCB layout for an Embedded Controller. The board is populated with various integrated circuits (ICs), including microcontrollers, memory chips, and peripheral components. The layout is organized into several functional blocks, each with its own set of components and connections.

Key Components and Connections:

- Microcontroller:** The central component is a microcontroller, likely an ARM-based device, connected to a multi-pin header (pins 1-30). It is surrounded by decoupling capacitors and power planes.
- Memory:** Several memory chips are present, including a 16MBIT DRAM (U10) and a 16MBIT DRAM (U11). They are connected to the microcontroller via address and data buses.
- Peripheral ICs:** Other components include a 74HC00 (U12), a 74VHC00 (U13), a 74VHC00 (U14), a 74VHC00 (U15), a 74VHC00 (U16), a 74VHC00 (U17), a 74VHC00 (U18), a 74VHC00 (U19), a 74VHC00 (U20), a 74VHC00 (U21), a 74VHC00 (U22), a 74VHC00 (U23), a 74VHC00 (U24), a 74VHC00 (U25), a 74VHC00 (U26), a 74VHC00 (U27), a 74VHC00 (U28), a 74VHC00 (U29), a 74VHC00 (U30), a 74VHC00 (U31), a 74VHC00 (U32), a 74VHC00 (U33), a 74VHC00 (U34), a 74VHC00 (U35), a 74VHC00 (U36), a 74VHC00 (U37), a 74VHC00 (U38), a 74VHC00 (U39), a 74VHC00 (U40), a 74VHC00 (U41), a 74VHC00 (U42), a 74VHC00 (U43), a 74VHC00 (U44), a 74VHC00 (U45), a 74VHC00 (U46), a 74VHC00 (U47), a 74VHC00 (U48), a 74VHC00 (U49), a 74VHC00 (U50), a 74VHC00 (U51), a 74VHC00 (U52), a 74VHC00 (U53), a 74VHC00 (U54), a 74VHC00 (U55), a 74VHC00 (U56), a 74VHC00 (U57), a 74VHC00 (U58), a 74VHC00 (U59), a 74VHC00 (U60), a 74VHC00 (U61), a 74VHC00 (U62), a 74VHC00 (U63), a 74VHC00 (U64), a 74VHC00 (U65), a 74VHC00 (U66), a 74VHC00 (U67), a 74VHC00 (U68), a 74VHC00 (U69), a 74VHC00 (U70), a 74VHC00 (U71), a 74VHC00 (U72), a 74VHC00 (U73), a 74VHC00 (U74), a 74VHC00 (U75), a 74VHC00 (U76), a 74VHC00 (U77), a 74VHC00 (U78), a 74VHC00 (U79), a 74VHC00 (U80), a 74VHC00 (U81), a 74VHC00 (U82), a 74VHC00 (U83), a 74VHC00 (U84), a 74VHC00 (U85), a 74VHC00 (U86), a 74VHC00 (U87), a 74VHC00 (U88), a 74VHC00 (U89), a 74VHC00 (U90), a 74VHC00 (U91), a 74VHC00 (U92), a 74VHC00 (U93), a 74VHC00 (U94), a 74VHC00 (U95), a 74VHC00 (U96), a 74VHC00 (U97), a 74VHC00 (U98), a 74VHC00 (U99), a 74VHC00 (U100).
- Power and Ground:** The board features a multi-layer power and ground plane. Power is supplied via a 3.3V regulator (U1) and a 5V regulator (U2). Ground is connected to a common ground plane.
- Signal Traces:** Signal traces are routed from the microcontroller to the peripheral ICs and the multi-pin header. The traces are labeled with pin numbers and signal names.

The board is labeled with "EMBEDDED CONTROLLER" and "16-Dec-2024". The layout is a professional engineering drawing, showing all components, their values, and their connections to the multi-pin header.

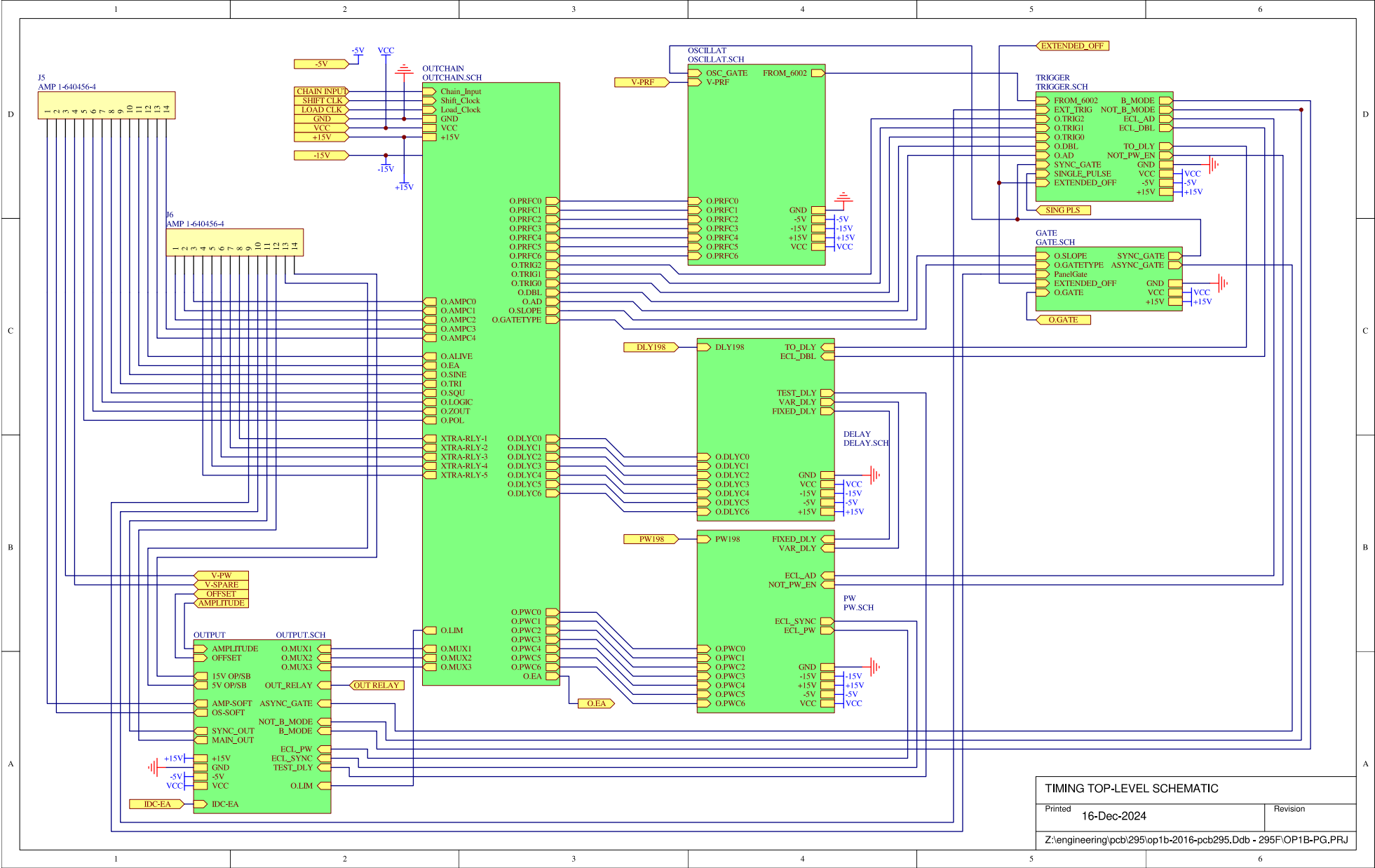
PCB 295F - MAIN CONTROL BOARD, 4 OF 13, GPIB INTERFACE



The diagram illustrates a PCB layout for a DAC system. The central component is the AD7839AS DAC, which is connected to a SN74ACT574PWR shift register. The DAC's outputs are buffered by LM348D op-amps (U45A, U45B, U45C, U45D, U46A, U46B, U46C, U46D). The shift register's outputs are also buffered by LM348D op-amps (U45E, U45F, U45G, U45H, U45I, U45J, U45K, U45L, U45M, U45N, U45O, U45P, U45Q, U45R, U45S, U45T, U45U, U45V, U45W, U45X, U45Y, U45Z). The layout includes power planes, decoupling capacitors, and signal traces. A table at the bottom right provides metadata for the design.

DACs		
Printed	16-Dec-2024	Revision 1
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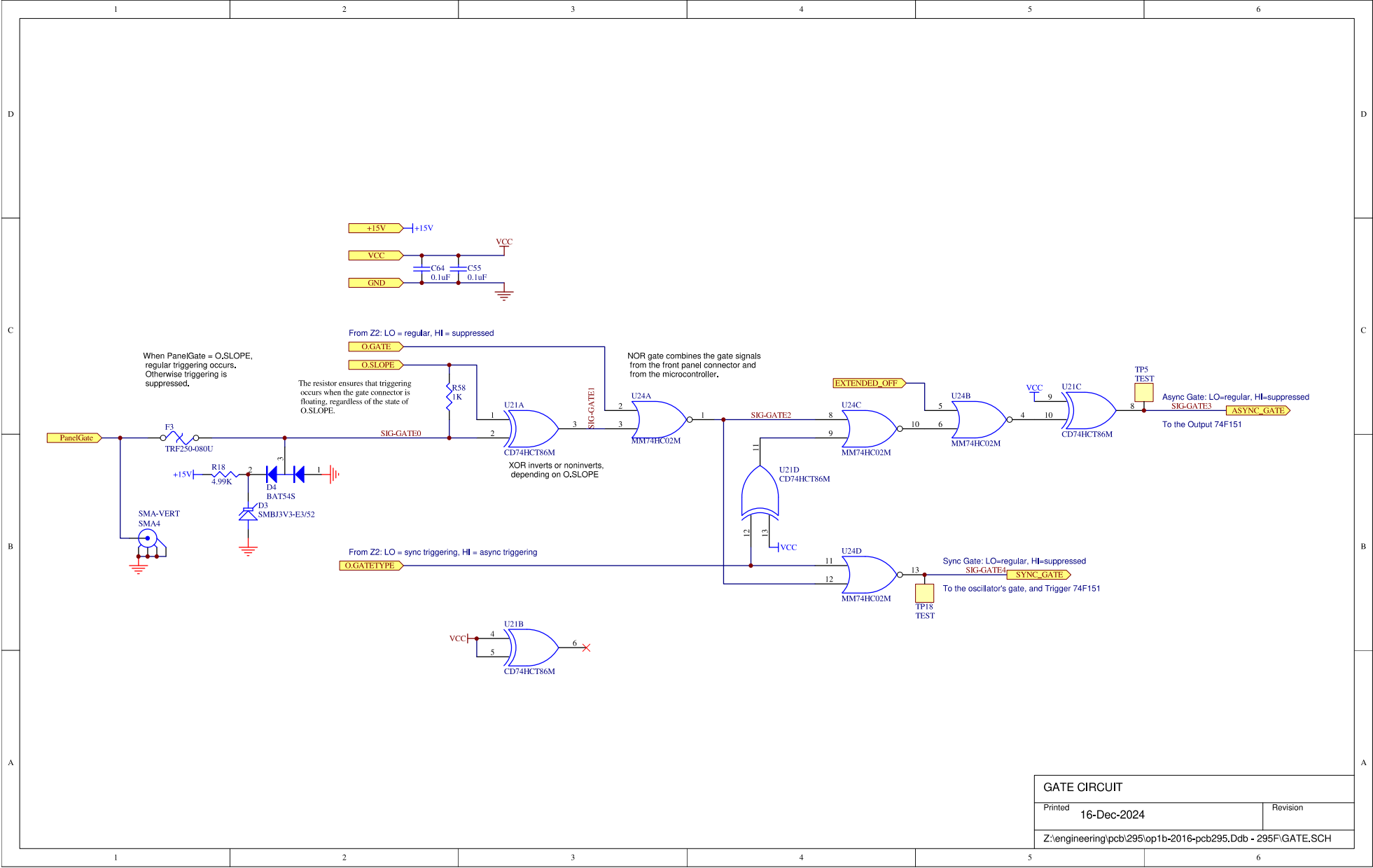
PCB 295F - MAIN CONTROL BOARD, 6 OF 13, TIMING TOP-LEVEL



TIMING TOP-LEVEL SCHEMATIC

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PCB 295F - MAIN CONTROL BOARD, 7 OF 13, GATE CIRCUIT



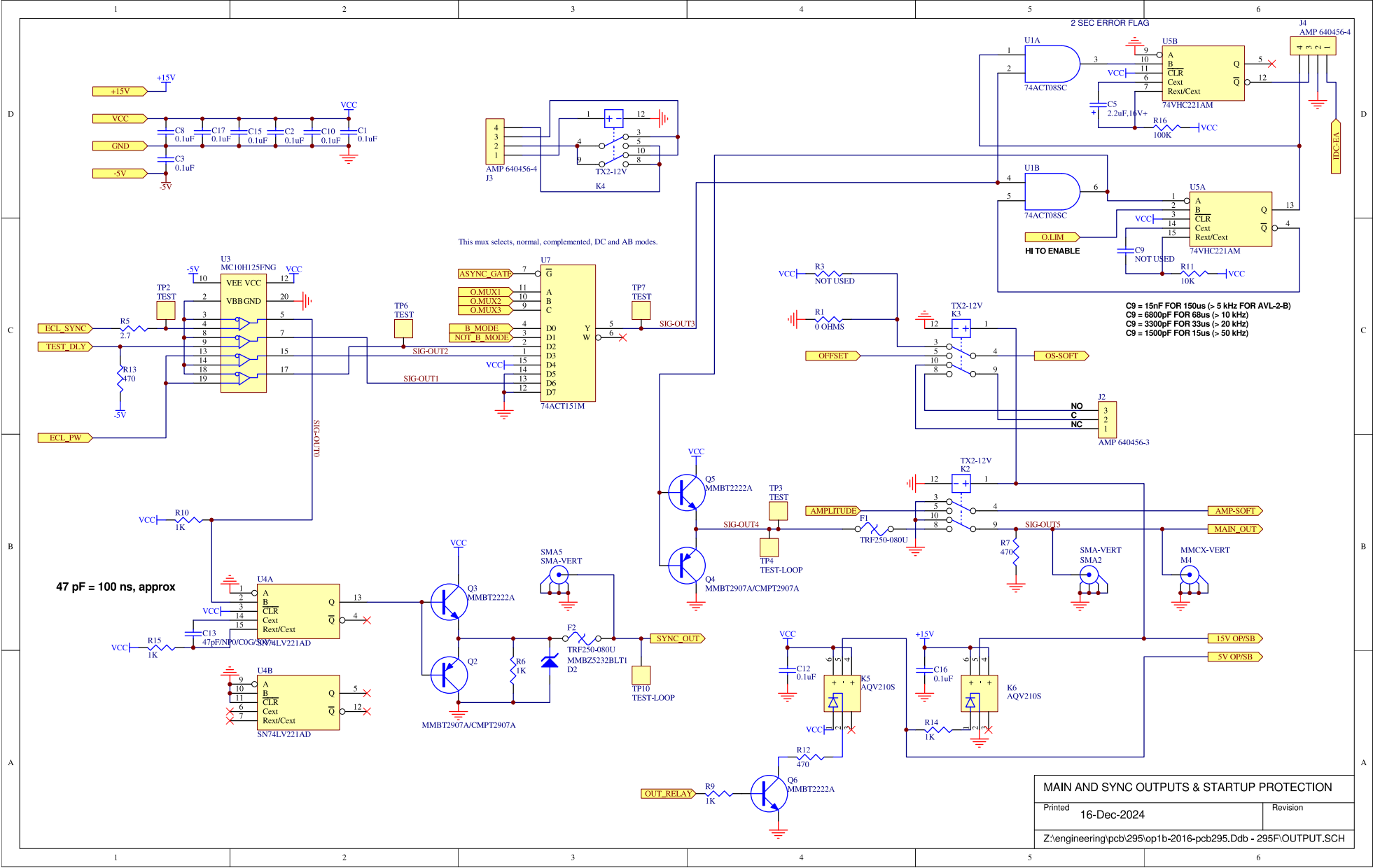
INTERNAL OSCILLATOR

Printed 16-Dec-2024

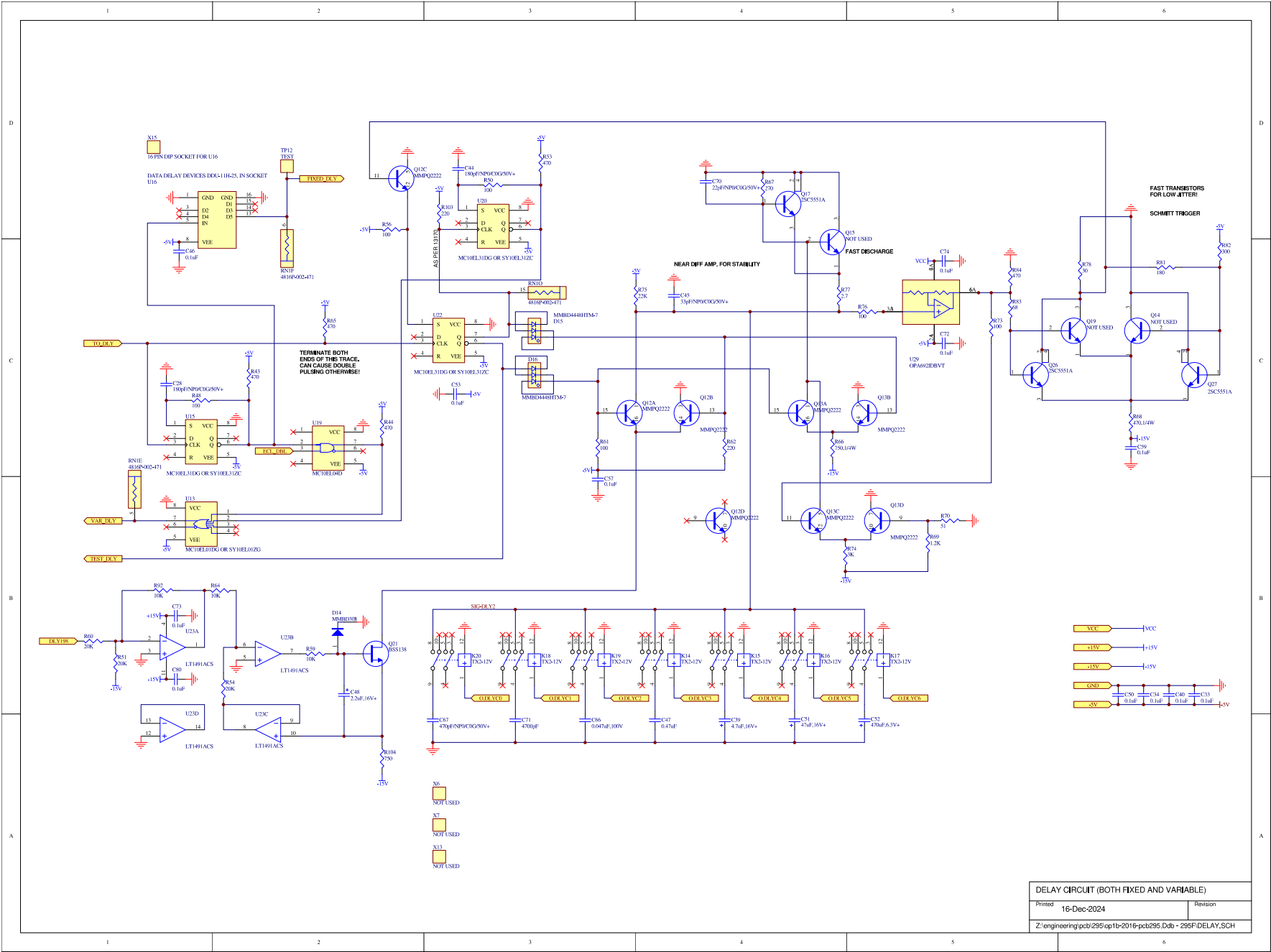
Revision

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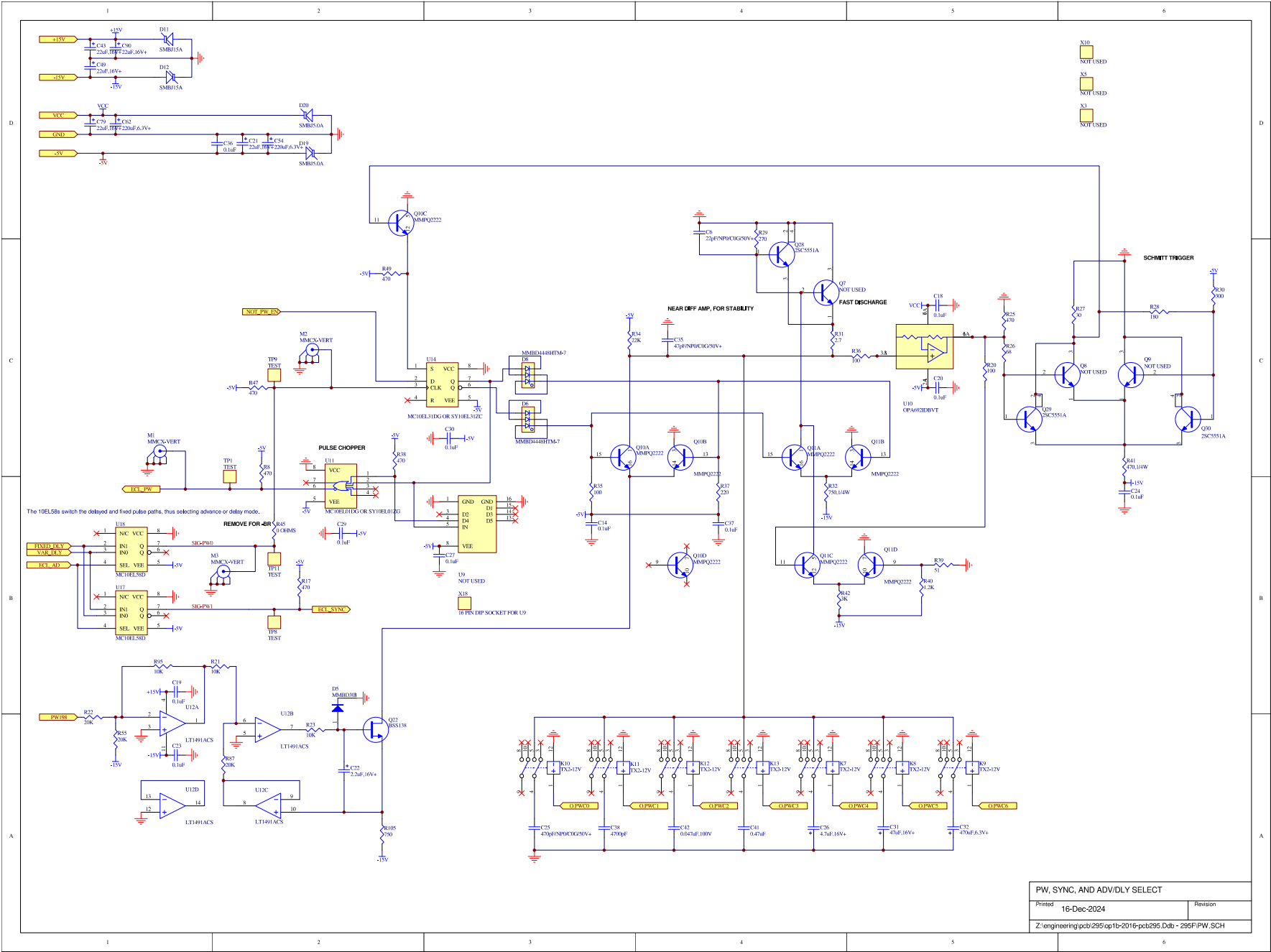
PCB 295F - MAIN CONTROL BOARD, 9 OF 13, TTL OUTPUTS



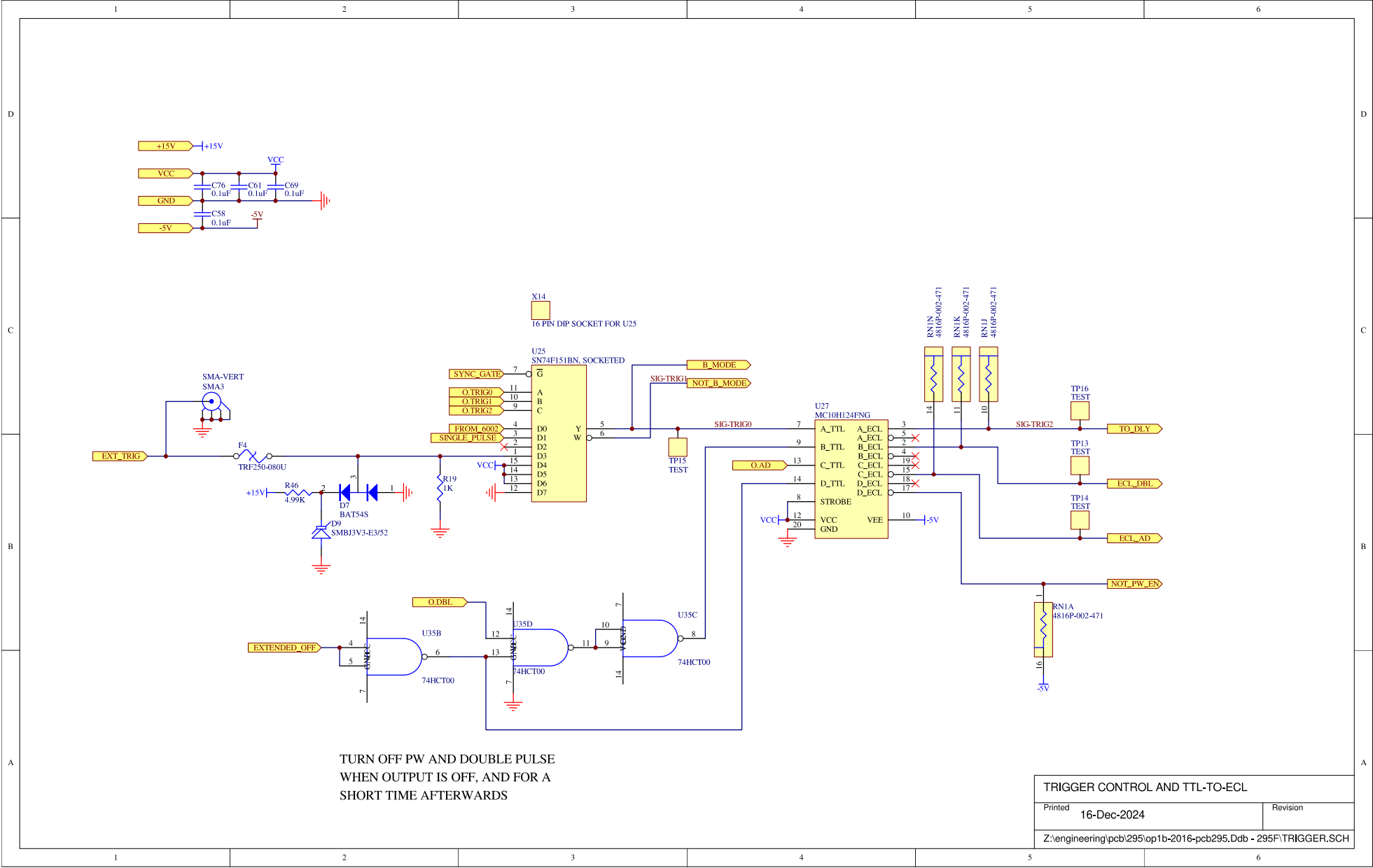
PCB 295F - MAIN CONTROL BOARD, 10 OF 13, DELAY CIRCUITS



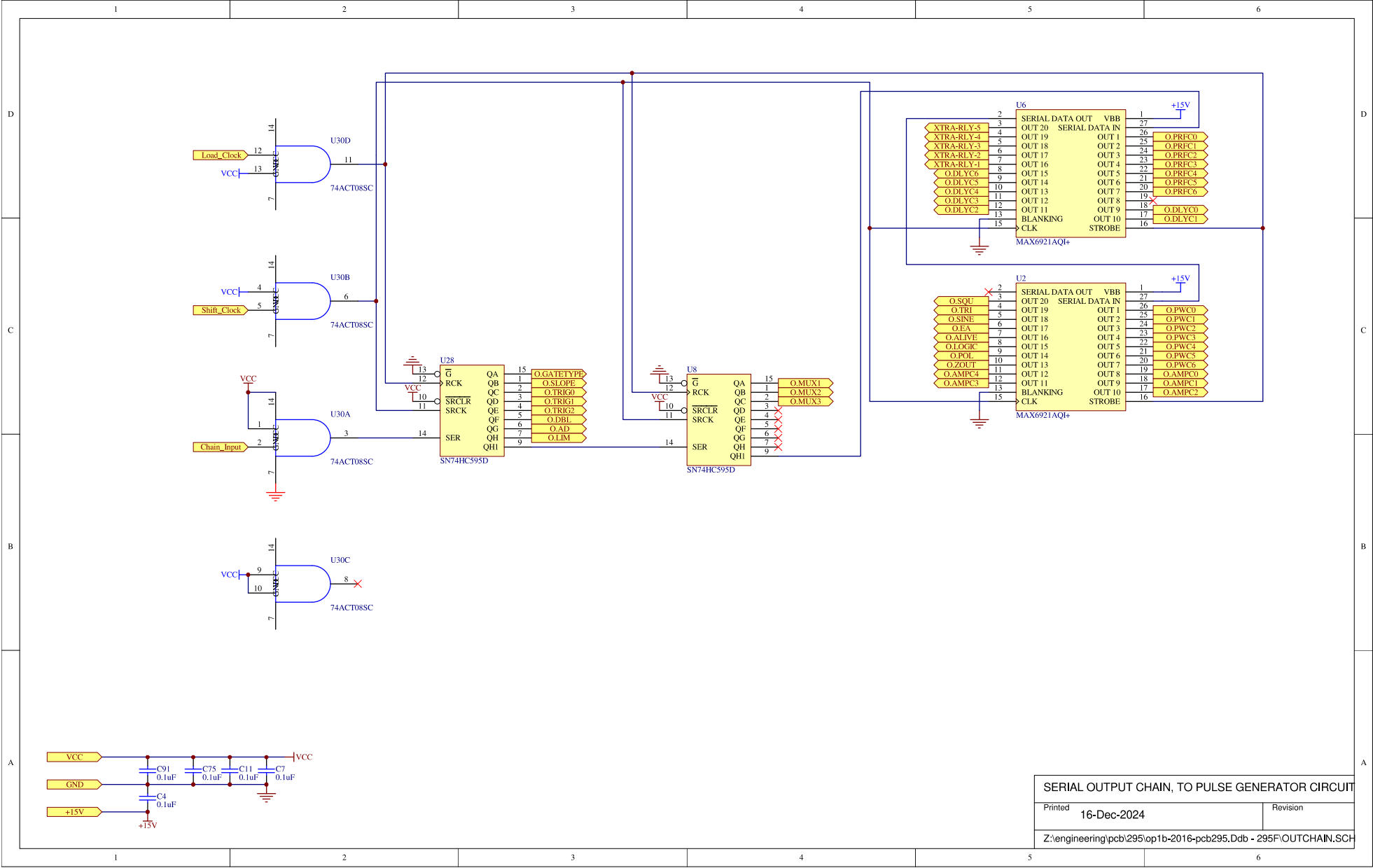
PCB 295F - MAIN CONTROL BOARD, 11 OF 13, PULSE WIDTH CIRCUIT



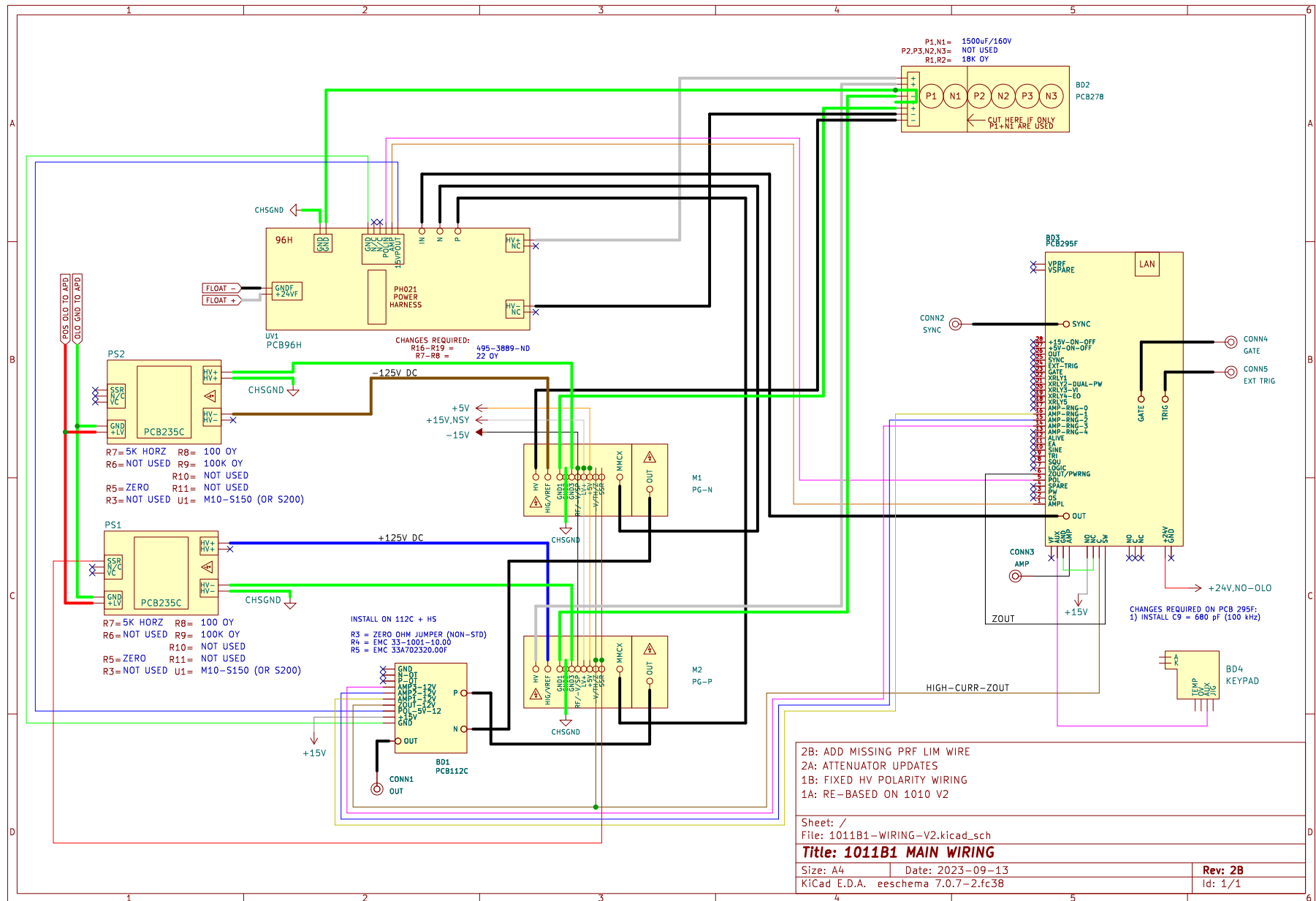
PCB 295F - MAIN CONTROL BOARD, 12 OF 13, TRIGGER SELECT



PCB 295F - MAIN CONTROL BOARD, 13 OF 13, SHIFT REGISTERS



MAIN WIRING



PERFORMANCE CHECK SHEET