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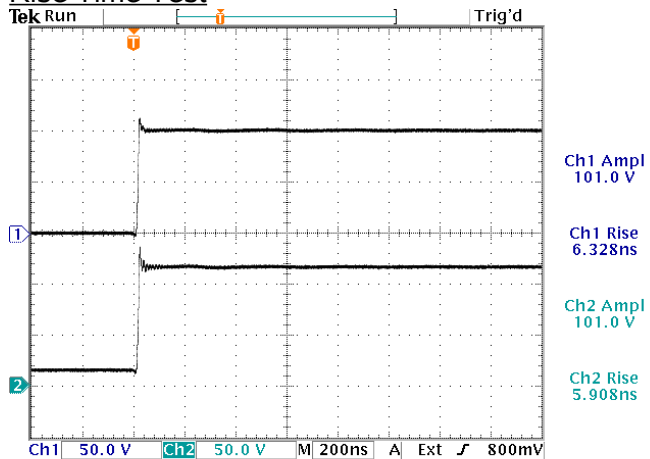
BOX 5120, LCD MERIVALE
OTTAWA, ONTARIO
CANADA K2C 3H5

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PERFORMANCE CHECKSHEET

Model: AVX-TD
Type: High-Speed Bias Tee
S.N.: 13643
Date: November 13, 2017

Rise Time Test

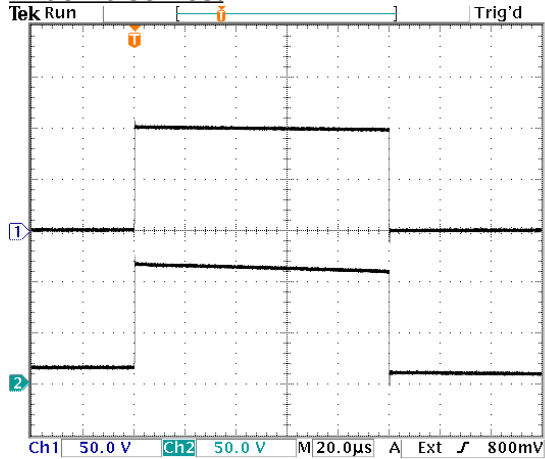


13 Nov 2017
15:26:08

Top: +100V, 100 us pulse from AVR-3HF-B-P-R5-AC03 S/N 13642.
Bottom: Output of AVX-TD, with +0.3A offset.

- a) Pulse/DC voltage limit: 100V
- b) DC current limit: 0.3A
- c) Pulse width range: 40 ns – 100 us
- d) Rise time (20%-80%): < 5 ns

Wide Pulse Test



13 Nov 2017
15:26:36

Top: +100V, 100 us pulse from AVR-3HF-B-P-R5-AC03 S/N 13642.
Bottom: Output of AVX-TD, with +0.3A offset.