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NANOSECOND WAVEFORM ELECTRONICS SINCE 1975

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## **INSTRUCTIONS**

## MODEL AVO-9E-C PULSE GENERATOR

S.N.:

#### WARRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been dissembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.

## **TECHNICAL SUPPORT**

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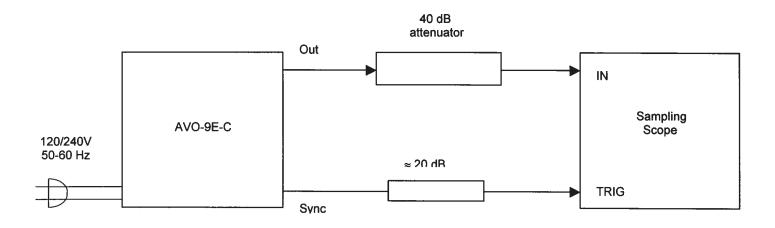
# TABLE OF CONTENTS

| WARRANTY   | . 2 |
|--|-----|
| TABLE OF CONTENTS  | . 3 |
| FIG. 1: PULSE GENERATOR TEST ARRANGEMENT<br>(AVX-S1 Module Disconnected) | . 5 |
| GENERAL OPERATING INSTRUCTIONS<br>(AVX-S1 Module Disconnected)           | . 6 |
| FIG. 2: PULSE GENERATOR TEST ARRANGEMENT<br>(AVX-S1 Module Connected)    | . 8 |
| GENERAL OPERATING INSTRUCTIONS<br>(AVX-S1 Module Connected)              | . 9 |
| AVX-S1 FUNCTIONAL EQUIVALENT CCT. PACKAGE 1                              | 10  |
| AVX-S SERIES DATA SHEET1   | 11  |
| SPECIFICATIONS   | 12  |
| FIG. 3: FRONT PANEL CONTROLS 1   | 13  |
| FRONT PANEL CONTROLS 1   | 14  |
| FIG. 4: BACK PANEL CONTROLS1   | 16  |
| BACK PANEL CONTROLS1   | 17  |
| FIG. 5: SYSTEM BLOCK DIAGRAM 1   | 18  |

| SYSTEM DESCRIPTION AND REPAIR     | . 19 |
|-----------------------------------|------|
| POWER SUPPLY AND FUSE REPLACEMENT | . 20 |
| PERFORMANCE CHECK SHEET           | . 22 |

Manual Reference: Q:\office\instructword\Avo-9\AVO-9E-Ceda-fig.doc, created August 29, 2000

# FIG. 1: PULSE GENERATOR TEST ARRANGEMENT (AVX-S1 Module Disconnected)



#### <u>GENERAL OPERATING INSTRUCTIONS</u> (AVX-S1 Module Disconnected)

- 1) The bandwidth capability of components and instruments used to display the pulse generator output signal (attenuators, cables, connectors, etc.) should exceed one gigahertz.
- 2) The use of 40 db attenuator at the sampling scope vertical input channel will insure a peak input signal to the sampling scope of less than one volt.
- 3) The sync output channel provides a +0.5 V pulse. To avoid overdriving the TRIG input channel of some sampling scopes, a 20 db attenuator should be placed at the input to the sampling scope trigger channel.
- 4) To obtain a stable output display the PRF and PRF FINE controls on the front panel should be set mid-range while the PRF range switch may be in either range. The front panel TRIG toggle switch should be in the INT position. The front panel DELAY control and the scope triggering controls are then adjusted to obtain a stable output. It is recommended that the DELAY control first be set max counter clockwise and then turned clockwise until a stable display is obtained. The scope may then be used to set the desired PRF by rotating the PRF and PRF FINE controls and by means of the PRF range switch.
- 5) The output pulse width is controlled by means of the front panel one turn PW control. The control should initially be set maximum counter clockwise and the pulse width adjusted using an oscilloscope.
- 6) To voltage control the output pulse width, remove the jumper wire between banana plugs A and B on the back panel and apply 0 to +10V to connector B (RIN > 10K). (EW option).
- 7) The output pulse amplitude is controlled by means of the front panel one turn AMP control.
- To voltage control the output amplitude, remove the jumper wire between banana plugs A and B on the back panel and apply 0 to +10V to connector B (RIN > 10K). (EA option).
- 9) An external clock may be used to control the output PRF of the AVO-9E unit by setting the front panel TRIG toggle switch in the EXT position and applying a 50 nsec, or under, TTL level pulse to the TRIG BNC connector input. For operation in this mode, the scope time base must also be triggered by the external clock rather than from the SYNC output.

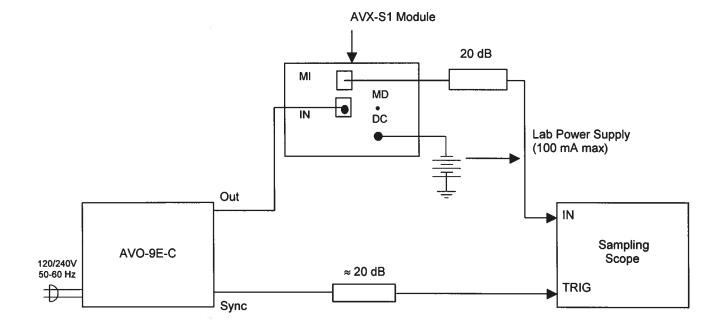
- 10) <u>WARNING</u>: Model AVO-9E-C may fail if triggered at a PRF greater than 10.0 MHz or at a duty cycle exceeding 20%.
- 11) AVO-9E-C units with a serial number higher than 5600 are protected by an automatic overload protective circuit which controls the front panel overload light. If the unit is overloaded (by operating at an exceedingly high duty cycle or by operating into a short circuit), the protective circuit will turn the output of the instrument OFF and turn the indicator light ON. The light will stay ON (i.e. output OFF) for about 5 seconds after which the instrument will attempt to turn ON (i.e. light OFF) for about 1 second. If the overload condition persists, the instrument will turn OFF again (i.e. light ON) for another 5 seconds. If the overload condition has been removed, the instrument will turn on and resume normal operation. Overload conditions may be removed by:
  - 1) Reducing PRF (i.e. switch to a lower range)
  - 2) Reducing pulse width (i.e. switch to a lower range).
  - 3) Removing output load short circuit (if any)
- 12) <u>Dual Polarity Option</u>. To invert the output of the AVO- 9E unit, connect the IN PORT of the AVX-3 unit to the OUT port of the AVO-9E mainframe. An inverted pulse is then obtained at the OUT port of the AVX-3 unit.
- 13) The rise and fall time are switched from the high to low range by means of the TR and TF two position switches. (TRF option).
- 14) The AVO-9E-C unit can be converted from 110 to 220V 50- 60 Hz operation by adjusting the voltage selector card in the rear panel fused voltage selector-cable connector assembly.
- 15) For additional assistance:

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# FIG. 2: PULSE GENERATOR TEST ARRANGEMENT (AVX-S1 Module Connected)



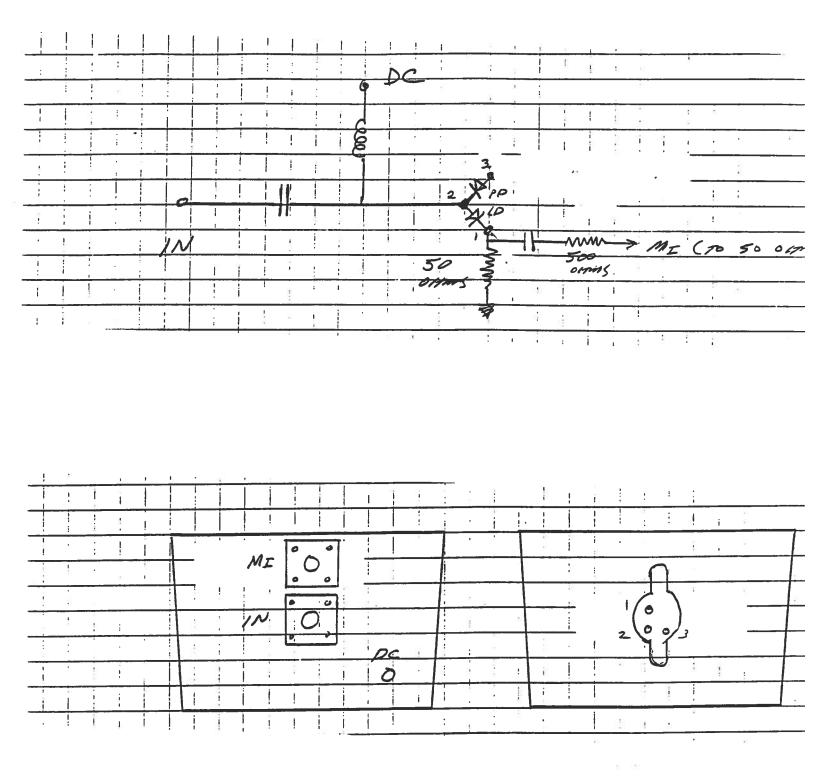
### GENERAL OPERATING INSTRUCTIONS (AVX-S1 Module Connected)

- 1) A general description of the AVX-S1 module is given in the enclosed data sheet.
- 2) The AVX-S1 module should be connected to the AVO-9E-C mainframe via the supplied 24" RG174 cable. The diode current may be monitored by connecting the MI output port to the sampling scope via a 20 db attenuator (MI option units only). The output amplitude (VMI, volts) and diode current (ID, Amp) are related as follows:

#### ID = 0.2 VMI

- 3) The laser diode plugs directly into the socket on the side of the AVX-S1 module.
- 4) A forward DC bias may be applied to the laser diode by connecting a DC potential of 0 to +5 volts to the DC solder terminal. The application of a small forward bias often yields a more ideal diode current waveform (as observed on the MI port). Note that the DC port must be shorted to ground if a bias is not applied.

# AVX-S1 FUNCTIONAL EQUIVALENT CCT. PACKAGE



10



|                |                  |             | 10 A<br>5 NS | MP/D<br>EC/D |      |      |      |      |
|----------------|------------------|-------------|--------------|--------------|------|------|------|------|
| <b>{-}-∳</b> - | <br>++++<br>ОРПС | -++++<br>AĽ | + + + +      |              | ++++ | ++++ | **** | +++- |
|                | OUTP             | <u>.</u>    |              |              |      |      |      |      |

The AVX-S series of bias insertion units is designed for applying pulse or RF CW signals and DC bias to laser diodes which insert into a high quality socket included on the mount. The bias insertion module includes the necessary networks to match the laser diode to the pulse or RF source as well as networks for applying DC bias to the diode. Optional outputs allow for monitoring of the laser diode current, voltage and a photo detector diode output. Readily available socket configurations (TO-18, TO-5, TO-3, OP-3) are shown on the following page. Note that the laser diodes are not supplied with the AVX-S series.

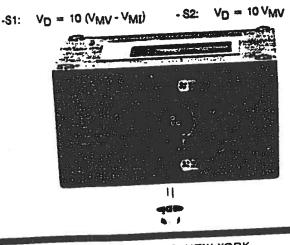
The AVX-S series includes 3 basic models namely the AVX-S1, AVX-S2 and the AVX-S3. The basic functional equivalent circuit for the three models are shown below. Model AVX-S1 is specifically designed for ultra high-speed, low current applications (rise times as low as 200 psec, bandwidths to 1 GHz, I < 1.0 ampere). Model AVX-S1 is employed in the AVO-9-C series of diode drivers. Model AVX-S2 is intended for application with rise times greater than 2 nsec and currents above 1 ampere. Model AVX-S3 is specifically designed for use with the AVO-2 and AVO-5 series pulse generators (which provide currents in the range of 5 to 50 amperes).

The input series blocking capacitor in Models AVX-S1 and AVX-S2 presents a low impedance to RF CW signals and to baseband pulses while the shunt indicator presents a high impedance to RF (or pulse) signals but an extremely low impedance to the DC blas. The resistor in series with the laser diode is selected to insure that the impedance at the IN port is 50 ohms. Normally a laser diode resistance of 3 ohms is assumed.

The optional diode current monitor ( $M_{I}$ ) provides an output waveform (to 50 ohms) which is an attenuated replica of the laser diode current. The output amplitude ( $V_{MI}$ , volts) and diode current ( $I_{O}$ , Amps) are related as follows:

-S1: 
$$I_D = 0.2V_{MI}$$
 -S2:  $I_D = V_{MI}$ 

The optional diode voltage monitor (MV) provides an output waveform that may be related to the voltage across the laser diode  $(V_D, volts)$  as follows:



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- Socket mounting of laser diodes
- Peak currents from 100 mA to 40 Amps
- Pulse widths from 0.4 to 200 nsec
- Rise times from 0.2 to 2.0 nsec
- Pulse or CW RF
- Diode current and voltage monitor options

Model AVX-S3 is available in four different versions (AVX-S3A, AVX-S3B, AVX-S3C and AVX-S3D) all of which include a matching trans former which effectively boosts the laser diode current beyond that provided by the pulse source.

Model AVX-S3A is designed to match 50 ohm pulse generators sucr as Model AVO-2-C to 12 ohm loads with peak currents of 5 amperes Consequently, the resistor RS in the equivalent circuit for this mode is 12 ohm. This resistor is accessible in all AVX-S3 models and ma be changed by the user (by desoldering). The series resistance c the laser diode and the resistor BS must equal the pulse generate source impedance divided by N<sup>2</sup>. Consequently, if the series resis tance of the laser diode is relatively high, it then may be necessar to reduce the value of Rs. Model AVX-S3B is designed to match  $\epsilon$ ohm pulse generators such as Model AVO-5-C to 3 ohms and w provide peak diode currents up to 28 amperes. Model AVX-S3C designed to match Models AVO-2W-C and AVO-2-C (25 ohm sourc impedance) to load resistance of about 5 ohms and will provic peak diode currents as high as 10 amperes. Model AVX-S3D designed for use with Model AVO-58-C and will provide up to " amperes of diode current.

Two optional SMA output connectors provide attenuated coincide replicas of the diode current (-MI option) and diode voltage (- $\hbar$  option) as per the following relationships (Amps, Volts):

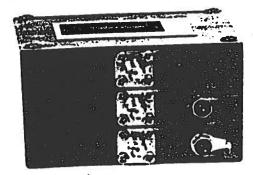
$$I_D = \frac{10 V_{MI}}{R_S}$$
  $V_D = 10 (V_{MV} - V_{MI})$ 

All AVX-S3 units include two foot long input cables with SMA ma

When ordering members of the AVX-S family, the customer mispecify the basic model number (eg. AVX-S1) and the followinadditional information.

- a) Diode package type (eg. TO-18) and the required pin conn tions (eg. anode, cathode, ground etc). See the following pa for readily available package mounting. Contact Avtech special or different packages.
- b) Desired options (eg. -MI, -MV, -MD).

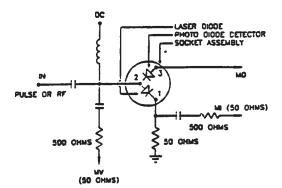
Contact Avtech for your special requirements.



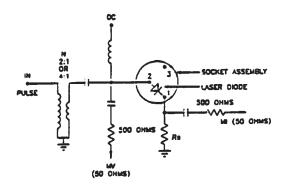
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## **SPECIFICATIONS**

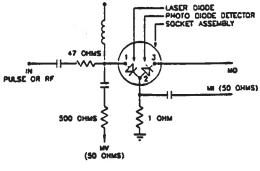
| Model:                | AVX-S1           | AVX-S2                     | AVX-S3A     | AVX-S3B   | AVX-S3C     | AVX-S3D    |  |  |
|-----------------------|------------------|----------------------------|-------------|-----------|-------------|------------|--|--|
| Peak diode current:   | 400 mA           | 2 Amps                     | 5 Amps      | 28 Amps   | 10 Amps     | 48 Amps    |  |  |
| Max. input amplitude: | 20 volts         | 100 voits                  | 150 voits   | 350 voits | 150 voits   | 150 volts  |  |  |
| Pulse width (nsec):   | 0.4 - 200        | 1 - 1000                   | 2 - 100     | 2 - 100   | 2 - 100     | 5 • 500    |  |  |
| Rise time (nsec):     | 0.2              | 0.5                        | 0.5         | 1.0       | 0.5         | 2.0        |  |  |
| Pulse PRF range:      | DC - 0.5 GHz     | DC - 100 MHz               | DC - 10 MHz | DC-10 MHz | DC - 10 MHz | DC - 1 MHz |  |  |
| CW frequency range:   | 10 MHz - 1.0 GHz | 1 - 200 MHz                | •           | e •       | •           | •          |  |  |
| Max. bias current:    | 100 mA           | 100 mA                     | 100 mA      | 100 mA    | 100 mA      | 100 mA     |  |  |
| Max. bias voltage:    | 50 voits         | 50 voits                   | 50 voits    | 50 voits  | 50 voits    | 50 voits   |  |  |
| Input impedance:      | 50 ohms          | 50 ohms                    | 50 ohms     | 50 ohms   | 25 ohms     | 12 ohms    |  |  |
| N:                    | •                | •                          | 2           | 4         | 2           | 4          |  |  |
| Rs (ohms):            | • 1              | •                          | 10          | 3         | 5           | 0.7        |  |  |
| IN connector:         |                  | SMA                        |             |           |             |            |  |  |
| Monitor connector:    |                  | SMA                        |             |           |             |            |  |  |
| Bias connector:       |                  | Solder pin                 |             |           |             |            |  |  |
| Size (in):            |                  | 1.6 x 2.6 x 3.0            |             |           |             |            |  |  |
| Materiai:             | 1                | Cast aluminum, blue enamei |             |           |             |            |  |  |
| Mounting:             | Any              |                            |             |           |             |            |  |  |





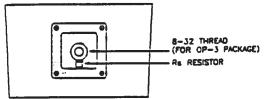


AVX-S3 FUNCTIONAL EQUIVALENT CIRCUIT

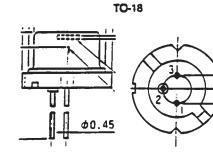


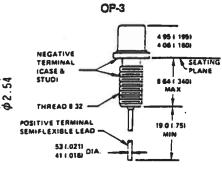
DC

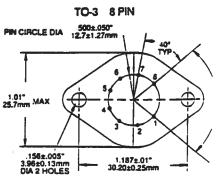




AVX-S3 INPUT ASSEMBLY (FOR OP-3 PACKAGE)

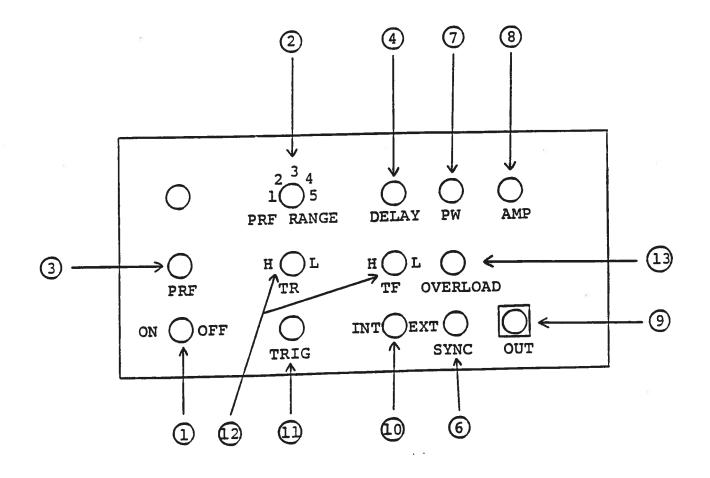






TYPICAL PACKAGES

## FIG. 3: FRONT PANEL CONTROLS



## FRONT PANEL CONTROLS

- (1) <u>ON-OFF Switch</u>. Applies basic prime power to all stages.
- (2) <u>PRF Control</u>. PRF RANGE, PRF and PRF FINE controls
- (3) determine output PRF as follows:

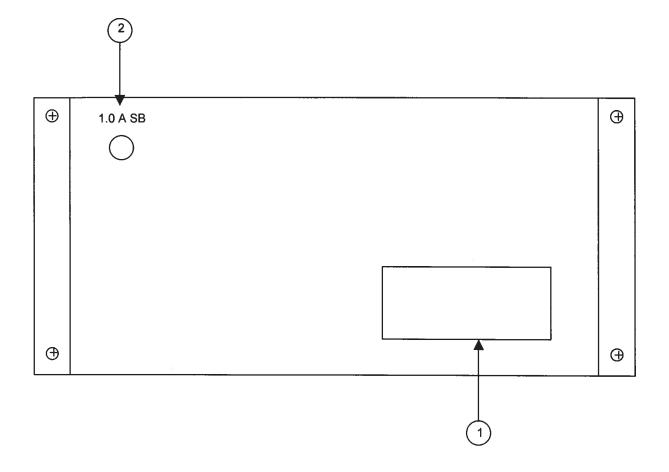
PRF MIN PRF MAX

| Range 1 | 200 Hz  | 2 KHz   |
|---------|---------|---------|
| Range 2 | 2 KHz   | 22 KHz  |
| Range 3 | 22 KHz  | 220 KHz |
| Range 4 | 180 KHz | 1.8 MHz |
| Range 5 | 1.2 MHz | 10 MHz  |

- (4) <u>DELAY Control</u>. Controls the relative delay between the reference output pulse provided at the SYNC output (6) and the main output (9). This delay is variable over the range of 0 to at least 100 nsec.
- (6) <u>SYNC Output</u>. This output precedes the main output (9) and is used to trigger the sampling scope time base. The output is a +0.5V 20 nsec (approx) pulse capable of driving a fifty ohm load.
- (7) <u>PW Control</u>. A one turn control which varies the output pulse width.
- (8) <u>AMP Control</u>. A one turn control which varies the output pulse amplitude from 0 to max output to a fifty ohm load.
- (9) <u>OUT Connector</u>. SMA connector provides output to a fifty ohm load.
- (10) <u>EXT-INT Control</u>. With this toggle switch in the INT position, the PRF of the unit is controlled via an internal clock which in turn is controlled by the PRF and PRF FINE controls. With the toggle switch in the EXT position, the unit requires a 50 nsec (or under) TTL level pulse applied at the TRIG input in order to trigger the output stages. In addition, in this mode, the scope time base must be triggered by the external trigger source.
- (11) <u>TRIG Input</u>. The external trigger signal is applied at this input when the EXT-INT toggle switch is in the EXT position.
- (12) <u>TR, TF, HIGH-LOW</u>. Two position switches to set TR (or TF) in HIGH and LOW ranges. (option TRF).

- (13) AVO-9E-C units with a serial number higher than 5600 are protected by an automatic overload protective circuit which controls the front panel overload light. If the unit is overloaded (by operating at an exceedingly high duty cycle or by operating into a short circuit), the protective circuit will turn the output of the instrument OFF and turn the indicator light ON. The light will stay ON (i.e. output OFF) for about 5 seconds after which the instrument will attempt to turn ON (i.e. light OFF) for about 1 second. If the overload condition persists, the instrument will turn OFF again (i.e. light ON) for another 5 seconds. If the overload condition has been removed, the instrument will turn on and resume normal operation. Overload conditions may be removed by:
  - 1) Reducing PRF (i.e. switch to a lower range)
  - 2) Reducing pulse width (i.e. switch to a lower range)
  - 3) Removing output load short circuit (if any)

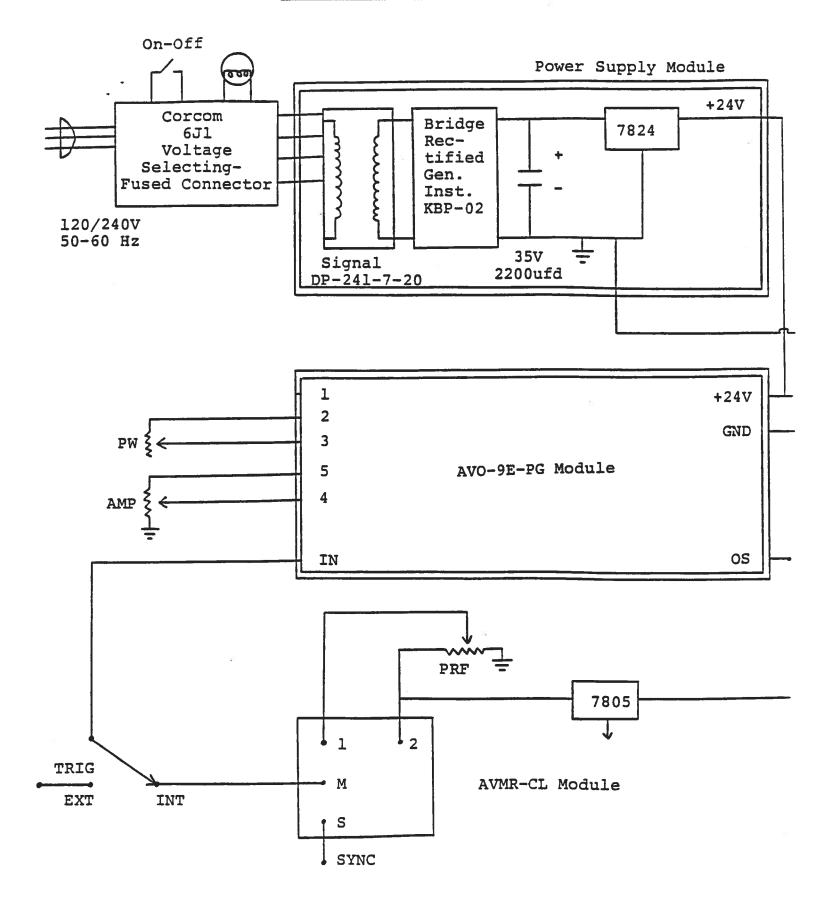
# FIG. 4: BACK PANEL CONTROLS



## BACK PANEL CONTROLS

- (1) <u>FUSED CONNECTOR, VOLTAGE SELECTOR</u>. The detachable power cord is connected at this point. In addition, the removable cord is adjusted to select the desired input operating voltage. The unit also contains the main power fuse (0.25 A SB).
- (2) <u>1.0A SB</u>. Fuse which protects the output stage if the output duty cycle rating is exceeded.

## FIG. 5: SYSTEM BLOCK DIAGRAM



#### SYSTEM DESCRIPTION AND REPAIR

The AVO-9E-C consists of a pulse generator module (AVO-9E- PG), a clock module (AVO-9E-CL) and a power supply board which supplies +24 volts (600 mA max) to the pulse generator module. In the event that the unit malfunctions, remove the instrument cover by removing the four Phillips screws on the back panel of the unit. The top cover may then be slid off. Measure the voltage at the +24 V pin of the PG module. If this voltage is substantially less than +24 volts, unsolder the line connecting the power supply and PG modules and connect 50 ohm 10 W load to the PS output. The voltage across this load should be about +24 V DC. If this voltage is substantially less than 24 volts the PS module is defective and should be repaired or replaced. If the voltage across the resistor is near 24 volts, then the PG module should be replaced or repaired. The sealed PG module must be returned to Avtech for repair (or replacement). The clock module provides a 20 nsec TTL level trigger pulse at pin M to trigger the PG module and a 20 nsec 0.5 V sync pulse at pin S to trigger the sampling scope display device. The output at pin S precedes the output at pin M by 0 to 100 nsec depending on the DELAY control setting. With the INT-EXT switch in the EXT position, the clock module is disconnected from the PG module. The clock module is functioning properly if:

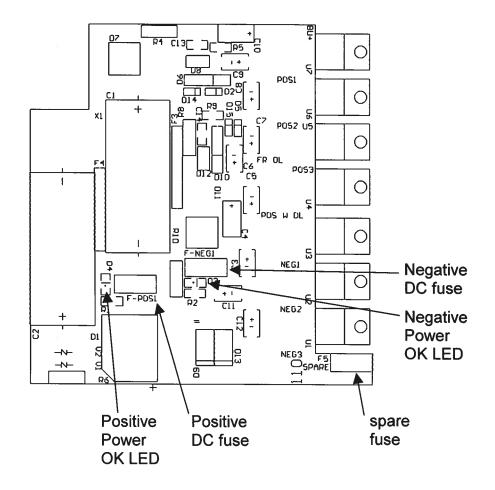
- 1) 20 nsec outputs are observed at pins M and S.
- 2) The PRF of the outputs can be varied over the range of 10 Hz to 10 MHz using the PRF, PRF FINE and PRF RANGE controls.
- 3) The relative delay between the pin M and S outputs can be varied by at least 100 nsec by the DELAY control.

The sealed clock module must be returned to Avtech for repair or replacement if the above conditions are not observed.

## POWER SUPPLY AND FUSE REPLACEMENT

This instrument has three fuses (plus one spare). One, which protects the AC input, is located in the rear-panel power entry module, as described in the "Rear Panel Controls" section of this manual. If the power appears to have failed, check the AC fuse first.

The other two fuses (plus one spare) are located on the internal DC power supply, as shown below:



The spare fuse may be used to replace one of the other fuses, if required.

The three fuses on this circuit board are 0.5A slow-blow fuses, Littlefuse part number R452.500. (This fuse can be ordered from Digikey, www.digikey.com. The Digikey part number is F1341CT-ND).

If you suspect that the DC fuses are blown, follow this procedure:

- 1. Remove the top cover, by removing the four Phillips screws on the top cover and then sliding the cover back and off.
- 2. Locate the two "Power OK" LEDs on the power supply circuit board, as illustrated above.
- 3. Turn on the instrument.
- 4. Observe the "Power OK" LEDs. If the fuses are not blown, the two LEDs will be lit (bright red). If one of the LEDs is not lit, the fuse next to it has blown.
- 5. Turn off the instrument.
- 6. If a fuse is blown, use needle-nose pliers to remove the blown fuse from its surface-mount holder.
- 7. Replace the fuse.

Aug 29/2000