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## INSTRUCTIONS

## MODEL AVO-9E-C-TRF PULSE GENERATOR

S.N.:

## WARRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been dissembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.

## TECHNICAL SUPPORT

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FIG. 1: PULSE GENERATOR TEST ARRANGEMENT (AVX-S2 MODULE DISCONNECTED)


## GENERAL OPERATING INSTRUCTIONS

1) The bandwidth capability of components and instruments used to display the pulse generator output signal (attenuators, cables, connectors, etc.) should exceed one gigahertz.
2) The use of 40 db attenuator at the sampling scope vertical input channel will insure a peak input signal to the sampling scope of less than one volt.
3) The TRIG output channel provides a $+0.5 \mathrm{~V}, 10$ ns pulse
4) To obtain a stable output display the PRF control on the front panel should be set mid-range while the PRF range switch may be in either range. The front panel TRIG toggle switch should be in the INT position. The front panel DELAY control and the scope triggering controls are then adjusted to obtain a stable output. It is recommended that the DELAY control first be set max counter clockwise and then turned clockwise until a stable display is obtained. The scope may then be used to set the desired PRF by rotating the PRF control and by means of the PRF range switch. CAUTION: Do not exceed the maximum duty cycle range of 20 dB .
5) The output pulse width is controlled by means of the front panel one turn PW control. The control should initially be set maximum counter clockwise and the pulse width adjusted using an oscilloscope.
6) To voltage control the output pulse width, set the rear panel switch in the EXT position and apply 0 to +10 V to BNC connector " A " ( $\left.\mathrm{R}_{\mathrm{IN}} \geq 10 \mathrm{~K}\right)$. (EW Option).
7) The output pulse amplitude is controlled by means of the front panel one turn AMP control.
8) To voltage control the output amplitude, set the rear panel switch in the EXT position and apply 0 to +10 V to BNC connector " B " $\left(\mathrm{R}_{\mathrm{IN}} \geq 10 \mathrm{~K}\right)$. (EW Option).
9) An external clock may be used to control the output PRF of the AVO-9E unit by setting the front panel TRIG toggle switch in the EXT position and applying a 50 ns , or under, TTL level pulse to the TRIG BNC connector input. For operation in this mode, the scope time base must also be triggered by the external clock rather than from the TRIG output.
10) WARNING: Model AVO-9E-C may fail if triggered at a PRF greater than 10.0 MHz or at a duty cycle exceeding $20 \%$.
11) Dual Polarity Option. To invert the output of the AVO-9E unit, connect the $I N$ PORT of the AVX-3 unit to the OUT port of the AVO-9E mainframe. An inverted pulse is then obtained at the OUT port of the AVX-3 unit.
12) The rise and fall times are switched from the high to low range by means of the TR and TF two position switches. (TRF option).
13) The monitor output (-M) provides a 20 dB attenuated coincident replica of the main output. (Option).
14) AVO-9E-C units with a serial number higher than 5600 are protected by an automatic overload protective circuit, which controls the front panel overload light. If the unit is overloaded (by operating at an exceedingly high duty cycle or by operating into a short circuit), the protective circuit will turn the output of the instrument OFF and turn the indicator light ON. The light will stay ON (i.e. output OFF) for about 5 seconds after which the instrument will attempt to turn ON (i.e. light OFF) for about 1 second. If the overload condition persists, the instrument will turn OFF again (i.e. light ON) for another 5 seconds. If the overload condition has been removed, the instrument will turn on and resume normal operation. Overload conditions may be removed by:
15) Reducing PRF (i.e. switch to a lower range)
16) Reducing pulse width (i.e. switch to a lower range)
17) The AVO-9E-C unit can be converted from 120 to $240 \mathrm{~V} 50-60 \mathrm{~Hz}$ operation by adjusting the voltage selector card in the rear panel fused voltage selectorcable connector assembly.
18) For additional assistance:

Tel: (613) 226-5772
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FIG. 2: PULSE GENERATOR TEST ARRANGEMENT (AVX-S2 MODULE CONNECTED)


## GENERAL DESCRIPTION

1) The AVX-S2 module should be connected to the pulse source mainframe via the supplied 24 " RG174 cable.
2) Gently insert the diode package loads into the 7 pin socket assembly. Pins 8 to 14 contact the AVX-S2 pin sockets. Pins 10 and 11 are the anode and cathode respectively, and pin 14 is one side of the "TEC". A direct connection to pin 14 is available at terminal 14 on the side of the AVX-S2 module.
3) Connect the MI port to a scope via 20 dB attenuator.
4) The diode package ma be secured in position by installing four 2-56 screws in the four tapped holes in the mounting ledge.
5) A forward DC bias may be applied to the laser diode by connecting a DC potential of 0 to +5 Volts to the DC solder terminal. The application of a small forward bias often yields a more ideal diode current waveform (as observed on the MI port). Note that the DC port must be shorted to ground if a bias is not applied.
6) The diode pulse current (Amps) and the Voltage at $M_{1}$ (Volts) are related as follows:

$$
I_{D}=0.2\left(V_{M I}-V_{M V}\right), V_{D}=10 V_{M V}
$$




The AVX-S series of bias insertion units is designed for applying pulse or RF CWsignals and DCblas to laser diodes which insert into a high quallity socket included on the mount. The bias insertion module includes the necessary networks to match the laser diode to the pulse or RF source as well as networks for applying OC bias to the diode. Optional outputs allow for monitoring of the laser diode current, voltage and a photo detector diode output. Readily available socket configurations (TO-18, TO-5, TO-3, OP-3) are shown on the following page. Note that the laser diodes are not supplied with the AVX-S series.
The AVX-S series includes 3 basic models namely the AVX-SI, AVX-S2 and the AVX-S3. The basic functional equivalent circuit for the three models are shown below. Model AVX-Si is specifically designed for ultra high-speed, low current applications (rise times as low as 200 psec, banctwidths to $1 \mathrm{GHz}, \mathrm{I}<1.0$ ampere). Model AVX-S1 is employed in the AVO-9-C series of diode drivers. Model AVX-S2 is intended for application with rise times greater than 2 nsec and currents above 1 ampere. Model AVX-S3 is specificatly designed for use with the AVO-2 and AVO-5 series pulse generators (which provide currents in the range of 5 to 50 amperes).
The input series blocking capacitor in Models AVX-S1 and AVX-S2 presents a low impedance to RF CW signals and to baseband pulses while the shunt indicator presents a high impedance to RF (or pulse) signals but an extremely low impedance to the DC blas. The resistor in series with the laser diode is setected to insure that the impedance at the $\mathbb{N}$ port is 50 ohms. Normally a laser diode resistance of 3 ohms is assumed.
The optional diode current monitor ( $M_{1}$ ) provides an output waveform (to 50 ohms) which is an attenuated replica of the laser diode current. The output amplitude NMI, volts) and diode current $^{\text {a }}$ ( $\mathrm{ID}_{\mathrm{D}}$ Amps) are related as follows:

$$
-S 1: \quad I_{D}=0.2 V_{M I} \quad S 2: \quad I D=V_{M I}
$$

The optional diode voltage monitor (MY) provides an output waveiorm that may be related to the voltage across the laser diode ND, volts) as follows:


- Socket mounting of laser diodes
- Peak currents from 100 mA to 40 Amps
- Pulse widths from 0.4 to 200 nsec
- Rise times from 0.2 to 2.0 nsec
- Pulse or CW RF
- Diode current and voltage monitor options

Model AVX-S3 is available in four different versions (AVX-S3A, AVXS3B. AVX-S3C and AVX-S3D) all of which Include a maiching transformer which effectively boosts the laser diode current beyond ìhat provided by the pulse source.
Model AVX-S3A is designed to match 50 ohm puise generators such as Model AVO-2-C to 120 hm loads with peak currents of 5 amperes. Consequently, the resistor Ris in the equivalent circuit for this model is 12 chm . This resiator is accessible in all AVX-S3 models and may be changed by the user (by desoldering). The series resistance of the laser diode and the resistor RS must equal the puise generator source impedance divided by $N^{2}$. Consequently, if the series resistance of the laser diode is relatively high, it then may be necessary to reduce the value of Rs. Model AVX-S3B is designed to mateh 50 ahm pulse generators such as Model AVO-5-C to 3 ohms and will provide peak diode currents up to 28 amperes. Model AVKS3C is designed to match Models AVO-2W-C and AVO-2-C (25 ohm source impedance) to load resistance of about 5 ohms and will provide peak diode currents as high as 10 amperes. Model AVX-S3D is designed for use with Model AVO-5B-C and will provide up to 48 ampares of diode current.
Two optional SMA output connectors provide attenuated coincident replicas of the diode current (-MI option) and diode vottage (-MV eption) as par the following retationships (Amps, Valts):

$$
I_{D}=\frac{10 V_{M I}}{R_{S}} \quad V_{D}=10\left(N_{M V} \cdot V_{M I}\right)
$$

All AVX-S3 units include two foot long input cables with SMA male connectors.
When ordering members of the AVX-S farnily, the customer must specity the basic model number (eg. AVX-SI) and the following edditional information.
a) Diode package type (eg. TO-18) and the required pin conneetions (eg. anocta, cathode, ground etc). See the following page for readily available package mounting. Contact Avech for special or different packages.
b) Desired options (eg. ML, -MV, MD).

Contact Avtech for your special requirements.


| Model: | AVX-Si | AVX-S2 | AVX-S3A | AVX-538 | AVX-S3C | AVK-530 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Peak diode current: | 400 mA | 2 Amps | 5 Amps | 28 Amps | 10 Amps | 48 Amps |
| Max. input amplituda: | 20 voits | 100 voits | 150 volts | 350 volts | 150 voits | 150 vatts |
| Puise width (nsec): | 0.4-200 | 1-1000 | 2-100 | 2-100 | 2-100 | $5 \cdot 500$ |
| Fise time (nsec): | 0.2 | 0.5 | 0.5 | 1.0 | 0.5 | 20 |
| Puise PRF range: | DC - 0.5 GHz | DC. 100 MHz | DC. 10 MHz | DC. 10 MHz | DC. 10 MHz | DC. 1 MHz |
| CW frequency ranas: | 10 MHz - 1.0 GHz | 1.200 MHz | - | - | - | - |
| Max. bias current: | 100 mA | 100 mA | 100 mA | 100 mA | 100 mA | 100 mA |
| Max. bias voltage: | 50 voits | 50 voits | 50 voits | 50 volts | 50 votts | 50 valts |
| Input impedance: | 50 ohms | 50 ohms | 50 ohms | 50 ohms | 250 nms | 12 atms |
| $\mathrm{N}:$ | - | - | 2 | 4 | 2 | 4 |
| Rs (ohms): | - | - | 10 | 3 | 5 | 0.7 |
| IN connector: | SMA |  |  |  |  |  |
| Monitor connector: | SMA |  |  |  |  |  |
| Bias connector: | Solder pin |  |  |  |  |  |
| Size (in): | $1.6 \times 2.6 \times 3.0$ |  |  |  |  |  |
| Material: | Cast aluminum, blue enamel |  |  |  |  |  |
| Mounting: | Any |  |  |  |  |  |



AVX-SI FUNCTONAL ERUNALENT CROUTI



AVX-SZ FUNCTIONAL EOUNALENT CIRCUTT


AVKS3 INPUT ASSEMELY (FOR OP-3 PACKAGEI


## FIG. 3: FRONT PANEL CONTROLS



## FRONT PANEL CONTROLS

(1) ON-OFF Switch. Applies basic prime power to all stages.
(2) PRF Control. PRF RANGE and PRF controls determine output PRF as follows:

|  | PRF MIN | PRF MAX |
| :--- | ---: | ---: |
| Range 1 |  | 100 Hz |
| Range 2 | 1 KHz | 10 KHz |
| Range 3 | 10 KHz | 100 KHz |
| Range 4 | 100 KHz | 1 MHz |
| Range 5 | 1 MHz | 10 MHz |

(3) DELAY Control. Controls the relative delay between the reference output pulse provided at the TRIG output (4) and the main output (7). This delay is variable over the range of 0 to at least 100 ns .
(4) TRIG Output. This output precedes the main output (7) and is used to trigger the sampling scope time base. The output is a +0.5 V 10 ns (approx.) pulse capable of driving a fifty-ohm load.
(5) PW Control. A one-turn control which varies the output pulse width.
(6) AMP Control. A one-turn control, which varies the output pulse amplitude from 0 to max output to a fifty-ohm load.
(7) OUT Connector. SMA connector provides output to a fifty-ohm load.
(8) EXT-INT Control. With this toggle switch in the INT position, the PRF of the unit is controlled via an internal clock, which in turn is controlled by the PRF and PRF FINE controls. With the toggle switch in the EXT position, the unit requires a 50 ns (or less) TTL level pulse applied at the TRIG input in order to trigger the output stages. In addition, in this mode, the scope time base must be triggered by the external trigger source.
(9) TR, TF, HIGH-LOW. Two position switches to set TR (or TF) in HIGH and LOW ranges. (Option TRF).
(10) OVERLOAD INDICATOR: AVO-9E-C units with a serial number higher than 5600 are protected by an automatic overload protective circuit which controls the front panel overload light. If the unit is overloaded (by operating at an
exceedingly high duty cycle or by operating into a short circuit), the protective circuit will turn the output of the instrument OFF and turn the indicator light ON. The light will stay ON (i.e. output OFF) for about 5 seconds after which the instrument will attempt to turn ON (i.e. light OFF) for about 1 second. If the overload condition persists, the instrument will turn OFF again (i.e. light ON) for another 5 seconds. If the overload condition has been removed, the instrument will turn on and resume normal operation. Overload conditions may be removed by:

1) Reducing PRF (i.e. switch to a lower range)
2) Reducing pulse width (i.e. switch to a lower range)

## FIG. 4: BACK PANEL CONTROLS



## BACK PANEL CONTROLS

(1) FUSED CONNECTOR, VOLTAGE SELECTOR. The detachable power cord is connected at this point. In addition, the removable cord is adjusted to select the desired input operating voltage. The unit also contains the main power fuse (0.5 A SB).
(2) EA. To voltage control the output amplitude, set the switch in the EXT position and apply 0 to +10 Volts to $B N C$ connector " $A$ " ( $R_{\text {IN }}>10 K$ ).
(3) EW. To voltage control the output pulse width, set the switch in the EXT position and apply 0 to +10 Volts to $B N C$ connector " $A^{\prime}\left(R_{I N}>10 \mathrm{~K}\right)$.

## COVER REMOVAL

The top cover may be removed by removing the four Philips screws on the top of the instrument. The top cover may then be slide back and off.

## POWER SUPPLY AND FUSE REPLACEMENT

This instrument has three fuses (plus one spare). One, which protects the AC input, is located in the rear-panel power entry module, as described in the "Rear Panel Controls" section of this manual. If the power appears to have failed, check the AC fuse first.

The other two fuses (plus one spare) are located on the internal DC power supply, as shown below:


The positive fuse and the spare fuse on this circuit board are 1 A slow-blow fuses, Littlefuse part number R452001. (This fuse can be ordered from Digikey, www.digikey.com. The Digikey part number is F1343CT-ND). The negative fuse is a 0.5A slow-blow fuse (Littlefuse R452.500, Digikey part number F1341CT-ND).

If you suspect that the DC fuses are blown, follow this procedure:

1. Remove the top cover, by removing the four Phillips screws on the top cover and then sliding the cover back and off.
2. Locate the two "Power OK" LEDs on the power supply circuit board, as illustrated above.
3. Turn on the instrument.
4. Observe the "Power OK" LEDs. If the fuses are not blown, the two LEDs will be lit (bright red). If one of the LEDs is not lit, the fuse next to it has blown.
5. Turn off the instrument.
6. If a fuse is blown, use needle-nose pliers to remove the blown fuse from its surface-mount holder.
7. Replace the fuse.

Oct. 3112000

