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INSTRUCTIONS

MODEL AVO-9A-C-R5 PULSE GENERATOR MODEL AVX-S1 BIAS INSERTION UNIT

8933 9437 S.N.:

WARRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been dissembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.

TECHNICAL SUPPORT

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Manual Reference: Q:\office\instructword\Avo-9\Avo-9a-c-edf.doc, created August 10, 1999



FIG. 1: PULSE GENERATOR TEST ARRANGEMENT (AVX-S1 MODULE REMOVED)

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GENERAL OPERATING INSTRUCTIONS

- 1) The bandwidth capability of components and instruments used to display the pulse generator output signal (attenuators, cables, connectors, etc.) should exceed ten gigahertz.
- 2) The use of 40 dB attenuator at the sampling scope vertical input channel will insure a peak input signal to the sampling scope of less than one Volt.
- 3) The TRIG output channel provides TTL level signals. To avoid overdriving the TRIG input channel of some sampling scopes, a 30 dB attenuator should be placed at the input to the sampling scope trigger channel.
- 4) To obtain a stable output display the PRF control on the front panel should be set mid-range while the PRF range switch may be in either range. The front panel TRIG toggle switch should be in the INT position. The front panel DELAY controls and the scope triggering controls are then adjusted to obtain a stable output. The scope may then be used to set the desired PRF by rotating the PRF control and by means of the PRF range switch.
- 5) The output pulse width is controlled by means of the front panel one turn PW control. The control should initially be set maximum clockwise and the pulse width adjusted using an oscilloscope.
- 6) The output pulse amplitude is controlled by means of the front panel one turn AMP control. The pulse width may change by several nanoseconds as the output amplitude is reduced from maximum to minimum. Therefore it is convenient to first set the desired amplitude and then set the desired pulse width. Rotation of the PW pot causes the position of the falling edge of the pulse to change.
- 7) Some properties of the output pulse may change as a function of the amplitude pot setting. For some demanding applications, it may be desirable to use a combination of external attenuators and the amplitude pot to achieve the desired output amplitude.
- 8) An external clock may be used to control the output PRF of the AVO unit by setting the front panel TRIG toggle switch in the EXT position and applying a 50 ns or wider. TTL level pulse to the TRIG BNC connector input. For operation in this mode, the scope time base must also be triggered by the external clock.

- 9) To voltage control the output pulse width, set the rear panel switch in the EXT position and apply 0 to +10V to connector A ($R_{IN} \ge 10K$). (EW option).
- 10) To voltage control the output amplitude, set the rear panel switch in the EXT position and apply 0 to +10V to connector B ($R_{IN} \ge 10K$). (EA option).
- 11) The unit can be converted from 120 to 240V 50-60 Hz operation by adjusting the voltage selector card in the rear panel fused voltage selector-cable connector assembly.
- 12) For additional assistance:

Tel: (613) 226-5772 Fax: (613) 226-2802 Email: info@avtechpulse.com

FIG. 2: PULSE GENERATOR TEST ARRANGEMENT (AVX-S1 MODULE CONNECTED)



CONNECTING THE AVO-9A-C TO THE AVX-S1

- 1) A general description of the AVX-S1 module is given in the enclosed data sheet.
- 2) The AVX-S1 module should be connected to the AVO-9A-C mainframe via the supplied 24" RG174 cable. The diode current may be monitored by connecting the MI and MV output ports to the sampling scope via 20 dB attenuators. The output amplitude (V_{MI} and V_{MV}, Volts) and diode current (I_D, Amp) are related as follows:

$$I_{\rm D} = 0.2 \, (V_{\rm MI} - V_{\rm MV})$$

The laser diode voltage is given by the following:

$$V_D = 10 V_{MV}$$

- 3) The laser diode plugs directly into the socket on the side of the AVX-S1 module.
- 4) A forward DC bias may be applied to the laser diode by connecting a DC potential of 0 to -5 Volts to the DC solder terminal. The application of a small forward bias often yields a more ideal diode current waveform (as observed on the MI port). Note that the DC port must be shorted to ground if a bias is not applied.

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LASER DIODE EIAS

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The AVX-S series of bias insertion units is designed for applying puise or RF CW signals and DC bias to taser diodes which insert into a high quality socket included on the mount. The bias insertion module includes the necessary networks to match the laser diode to the puise or RF source as well as networks for applying DC bias to the diode. Optional outputs allow for monitoring of the laser diode current, voltage and a photo detector diode output. Readily available socket configurations (TO-18, TO-5, TO-3, OP-3) are shown on the following page. Note that the laser diodes are not supplied with the AVX-S series.

The AVX-S series includes 3 basic models namely the AVX-S1. AVX-S2 and the AVX-S3. The basic functional equivalent circuit for the three models are shown below. Model AVX-S1 is specifically designed for ultra high-speed, low current applications (rise times as low as 200 psec, bandwidths to 1 GHz, [<1.0 ampere). Model AVX-S1 is employed in the AVO-9-C series of diode drivers. Model AVX-S2 is intended for application with rise times greater than 2 nsec and currents above 1 ampere. Model AVX-S3 is specifically designed for use with the AVO-2 and AVO-5 series pulse generators (which provide currents in the range of 5 to 50 amperes).

The input series blocking capacitor in Models AVX-S1 and AVX-S2 presents a low impedance to RF CW signals and to baseband pulses while the shunt indicator presents a high impedance to RF (or pulse) signals but an extremely low impedance to the OC bins. The resistor in series with the laser cliode is selected to insure that the impedance at the IN port is 60 chms. Normally a laser cliode resistance of 3 chms is assumed.

The optional clock current monitor (M_D provides an output waveform (to 50 ohms) which is an attenuated replica of the laser clock current. The output amplitude (V_{ML} volts) and clock current (I_D , Amps) are related as follows:

$$s_1: I_0 = 0.2V_{MI}$$
 $-s_2: I_0 = V_{MI}$

The optional clode voltage monitor (MV) provides an output waveform that may be related to the voltage across the laser clode (V_0, volts) as follows:

-S1: $V_{D} = 10 (V_{MV} - V_{MI})$ -S2: $V_{D} = 10 V_{MV}$



- Socket mounting of laser diodes
- Peak currents from 100 mA to 48 Amps
- Pulse widths from 0.4 to 200 nsec
- Rise times from 0.2 to 2.0 nsec
- Pulse or CW RF
- Olode current and voltage monitor options

Model AVX-S3 is available in four different versions (AVX-S3A, AVX-S3B, AVX-S3C and AVX-S3D) all of which include a matching transformer which effectively boosts the laser diode current beyond that provided by the guise source.

Model AVX-S3A is designed to match S0 ohm pulse generators such as Model AVO-2-C to 12 ohm loads with peak currents of 5 amperes. Consequently, the resistor Rg in the equivalent circuit for this model is 10 chm. This resistor is accessible in all AVX-S3 models and may be changed by the user (by desoldering). The series resistance of the laser diode and the resistor $f_{\rm S}$ must equal the pulse generator source impedance divided by N⁴. Consequently, if the series resistance of the laser clode is relatively high, it then may be necessary to reduce the value of Fig. Model AVX-S3B is designed to match 50 ohm pulse generators such as Model AVO-5-C to 3 chms and will provide peak clickle currents up to 28 amperes. Model AVX-S3C is signed to match Models AVO-2W-C and AVO-2-C (25 chm source impedance) to load resistance of about 5 chms and will provide peak diode currents as high as 10 amperes. Model AVX-S3D is migned for use with Model AVO-58-C and will provide up to 48 ampares of diode current.

Two optional SMA output connectors provide attenuated coincidem reptices of the cliptic current (-MI option) and cliptic voltage (-MV option) as per the following relationships (Amps, Volta):

$$I_0 = \frac{10 V_{MI}}{H_8}$$
 $V_0 = 10 (V_{MV} - V_{MI})$

All AVX-S3 units include two foot long input cables with SMA male connectors.

When ordering members of the AVX-S family, the customer must specify the basic model number (eg. AVX-S1) and the following additional information.

- a) Close package type (eg. TC-18) and the required pin connections (eg. anode, cathode, ground etc). See the following page for readily available package mounting. Contact Avtech for special or different packages.
- b) Desired options (eg. -MI, -MV, -MD).

Contact Avtech for your special requirements.

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		Specific	ATIONS		AVX	(-S S
Model:	AVX-S1	AVX-52	AVX-S3A	AVX-S3B	AVX-S3C	AV)
	400 mA	2 Amos	5 Amos	28 Amos	10 Amos	487
Peak diode cuiterit:	20 voite	100 volts	150 volts	350 voits	150 votts	150
Max. inout amontude:	04.200	1 . 1000	2-100	2-100	2-100	5-
Pulse width (nsec):	02	05	0.5	1.0	0.5	
Rise time insect:			OC . 10 MHz	OC-10 MHz	OC - 10 MHz	OC -
Puise PRF range:	UG-US GHZ					
CW frequency range:	10 MHz - 1.0 GHz	1 - 200 MHX	•	100	100	100
Max, bias current:	100 mA	100 mA	100 mA	100 mA	100 ///04	
Max, biss voltage:	ja 50 voite	50 voits	50 voits	50 voits	50 volts	50
logut impedance:	50 onms	50 onms	50 chms	50 onms	25 chms	12.0
N-	-	•	2	4	2	
Re (chms):	•	•	10	3	5	0
IN connector:	3	SMA				
Monitor connector:	SMA					
Bias connector:	Solder cin					
Size (in):	1.6 x 2.6 x 3.0					
Materiai:	Cast aluminum, blue ename:					
Mounting:	Any					



AVX-ST FUNCTIONAL EQUIVALENT CIRCUIT



AVX-52 FUNCTIONAL EQUIVALENT CIRCUIT



AVX-S3 FUNCTIONAL EQUIVALENT CIRCUIT



AVX-53 INPUT ASSEMBLY (FOR OP-3 PACKAGE)

4 95 1 1951 4 85 6 1989

19 6 (75)





TYPICAL PACKAGES

oc AVX-S S





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FRONT PANEL CONTROLS

- (1) <u>ON-OFF Switch</u>. Applies basic prime power to all stages.
- (2) <u>PRF Control</u>. The PRF RANGE and PRF controls determine output PRF as follows:

PRF MIN PRF MAX

Range 1	100 Hz	1 kHz
Range 2	1 kHz	10 kHz
Range 3	10 kHz	100 kHz
Range 4	100 kHz	1 MHz

- (3) <u>DELAY Controls</u>. Controls the relative delay between the reference
 i. output pulse provided at the TRIG output (6) and the main output (9). This delay is variable over the range of 0 to at least 500 ns.
- (4) <u>TRIG Output</u>. This output precedes the main output (9) and is used to trigger the sampling scope time base. The output is a TTL level 100 ns (approx.) pulse capable of driving a fifty Ohm load.
- (5) <u>PW Control</u>. A one turn control which varies the output pulse width from 0 to 4 ns.
- (6) <u>AMP Control</u>. A one turn control which varies the output pulse amplitude.
- (7) <u>OUT</u>. SMA connector provides output to 50 Ohm load.
- (8) <u>EXT-INT Control</u>. With this toggle switch in the INT position, the PRF of the AVO unit is controlled via an internal clock which in turn is controlled by the PRF controls. With the toggle switch in the EXT position, the AVO unit requires a 50 ns (or wider) TTL level pulse applied at the TRIG input in order to trigger the output stages. In addition, in this mode, the scope time base must be triggered by the external trigger source.

FIG. 4: BACK PANEL CONTROLS



BACK PANEL CONTROLS

(1) <u>FUSED CONNECTOR, VOLTAGE SELECTOR</u>. The detachable power cord is connected at this point. In addition, the removable cord is adjusted to select the desired input operating voltage. The unit also contains the main power fuse (0.5 Amp).

COVER REMOVAL

Top cover may be removed by removing the 4 Philips screws on the top of the instrument. The top cover may then be slid back and off.

-R5 RACK MOUNT KIT

The -R5 rack mount kit may be installed after first removing the one Philips screw on the side panel adjacent to the front handle.

This instrument has three fuses (plus one spare). One, which protects the AC input, is located in the rear-panel power entry module, as described in the "Rear Panel Controls" section of this manual. If the power appears to have failed, check the AC fuse first.

The other two fuses (plus one spare) are located on the internal DC power supply, as shown below:



The spare fuse may be used to replace one of the other fuses, if required.

The three fuses on this circuit board are 0.5A slow-blow fuses, Littlefuse part number R452.500. (This fuse can be ordered from Digikey, www.digikey.com. The Digikey part number is F1341CT-ND).

If you suspect that the DC fuses are blown, follow this procedure:

- 1. Remove the top cover, by removing the four Phillips screws on the top cover and then sliding the cover back and off.
- 2. Locate the two "Power OK" LEDs on the power supply circuit board, as illustrated above.
- 3. Turn on the instrument.
- 4. Observe the "Power OK" LEDs. If the fuses are not blown, the two LEDs will be lit (bright red). If one of the LEDs is not lit, the fuse next to it has blown.
- 5. Turn off the instrument.
- 6. If a fuse is blown, use needle-nose pliers to remove the blown fuse from its surface-mount holder.
- 7. Replace the fuse.