

INSTRUCTIONS

MODEL AVMP-2-N-TERA1 PULSE GENERATOR

S.N.:

WARRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been disassembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.

TECHNICAL SUPPORT

Phone: 613-226-5772 or 1-800-265-6681

Fax: 613-226-2802 or 1-800-561-1970

E-mail: info@avtechpulse.com

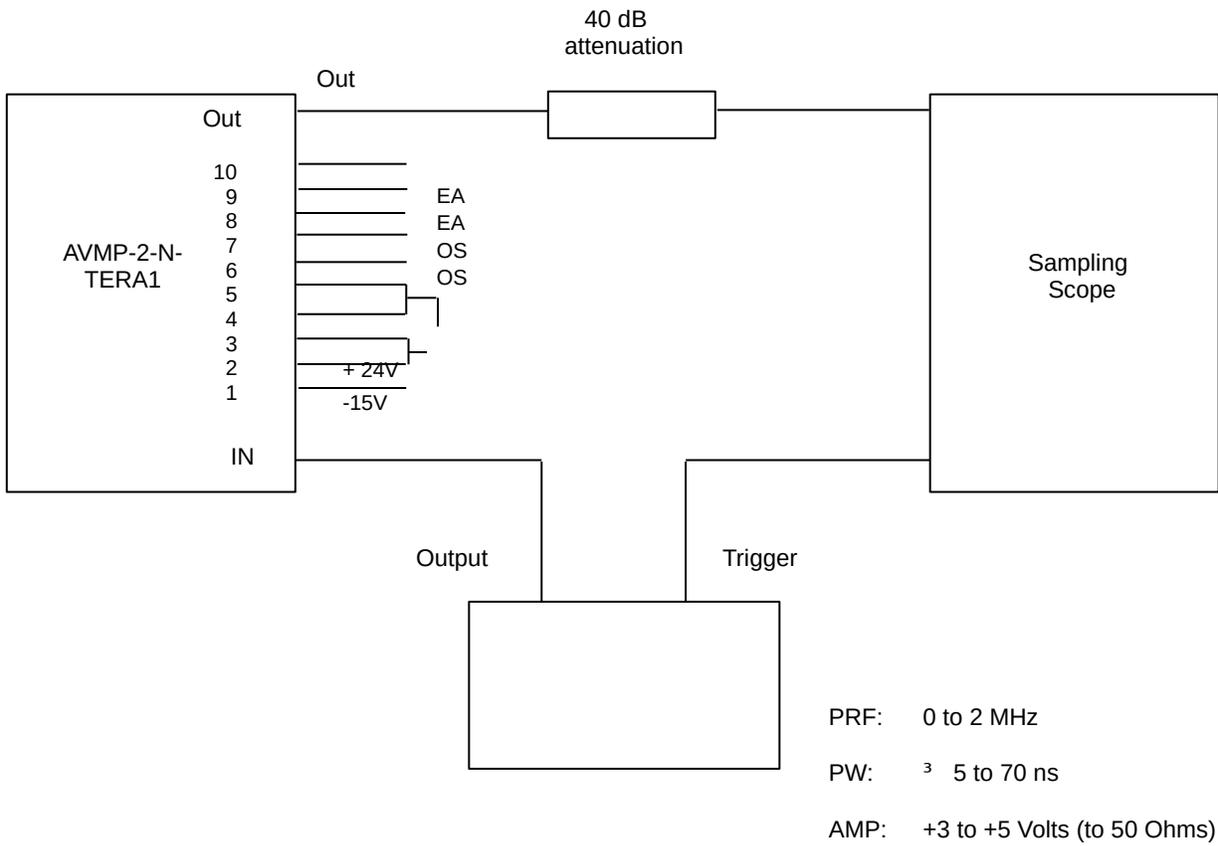
World Wide Web: <http://www.avtechpulse.com>

TABLE OF CONTENTS

WARRANTY.....	2
FIG. 1: PULSE GENERATOR TEST ARRANGEMENT.....	5
GENERAL OPERATING INSTRUCTIONS.....	6
ORIGINAL COPY OF QUOTE.....	7
PERFORMANCE CHECK SHEET.....	10

Manual Reference: /fileserver1/officefiles/instructword/avmp/AVMP-2-N-TERA1.doc, edition1, created November 29, 2002

FIG. 1: PULSE GENERATOR TEST ARRANGEMENT



GENERAL OPERATING INSTRUCTIONS

- 1) The bandwidth capability of components and instruments used to display the pulse generator output signal (attenuators, cables, connectors, etc.) should exceed ten gigahertz.
- 2) The use of 40db attenuator will insure a peak input signal to the sampling scope of less than one volt.
- 3) In general, the source pulse generator trigger delay control should be set in the 0.1 to 1.0 us range.
- 4) The DC control voltages and the DC prime power supplies are applied to the unit via the amp 1-640441-0 connector (which is supplied along with a 59803-1 installation tool). The connector mates to 24 AWG.
CAUTION: When connecting the connector to the chassis always insure that pin 1 on the connector aligns with and mates with pin 1 on the chassis. The unit may be damaged if this alignment is not achieved. Such damage is not covered by the warranty.
- 5) The DC prime power of +24VDC and -15V are respectively connected to pins 2,3 and to pin 1.
- 6) The output pulse width is equal to the input trigger pulse width (approx). The input impedance at the "IN" SMA control is 50 Ohms.
- 7) The output pulse amplitude is controlled by means of 0 to +10 VDC applied to pin 8 or 9. The pulse width may change by several nanoseconds as the output amplitude is reduced from maximum to minimum. Therefore it is convenient to first set the desired amplitude and then set the desired pulse width.
- 8) To DC offset the output pulse connect a DC power supply set to the required DC offset value pins 6 or 7. The maximum DC offset voltage is 50 volts (200 mA).
- 9) WARNING: Model AVMP-2-N-TERA1 may fail if triggered at a PRF greater than 2.0 MHz or at a duty cycle exceeding 10%.
- 10) For additional assistance:

Tel: 613-226-5772
Fax: 613-226-2802
Email: info@avtechpulse.com

ORIGINAL COPY OF QUOTE

PERFORMANCE CHECK SHEET