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SINCE 1975

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INSTRUCTIONS

MODEL AVMP-2

0 TO 10 VOLTS, 1 MHz

HIGH SPEED PULSE GENERATOR MODULE

WITH 100 ps RISE TIME

SERIAL NUMBER: _____

WARRANTY

Avtech Electrosystems Ltd. warrants products of its manufacture to be free from defects in material and workmanship under conditions of normal use. If, within one year after delivery to the original owner, and after prepaid return by the original owner, this Avtech product is found to be defective, Avtech shall at its option repair or replace said defective item. This warranty does not apply to units which have been disassembled, modified or subjected to conditions exceeding the applicable specifications or ratings. This warranty is the extent of the obligation assumed by Avtech with respect to this product and no other warranty or guarantee is either expressed or implied.

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Manual Reference: /files/server1/officefiles/instructword/avmp/AVMP-2,ed2.odt.
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INTRODUCTION

The AVMP-2 is a high performance DC-powered module capable of generating up to 10V into 50 Ω loads at repetition rates up to 1 MHz. The output pulse width is variable from 5 to 100 ns. The rise time is less than 100 ps, and the fall time is less than 135 ps.

Instruments with the "-P" model suffix can generate 0 to +10V, whereas instruments with the "-N" model suffix can generate 0 to -10V.

The AVMP-2 must be triggered by an external TTL pulse applied to the "IN" connector.

The output is designed to drive 50 Ω loads. (A 50 Ω load is required for proper operation.) The output is AC-coupled.

This instrument is intended for use in research and development laboratories.

AVAILABLE OPTIONS

The AVMP-2 is available with several options:

-ECL Option: the input trigger levels are ECL, rather than TTL.

-EA Option: the output amplitude can be controlled by an externally generated 0 to +10V analog control voltage.

-EW Option: the output pulse width can be controlled by an externally generated 0 to +10V analog control voltage.

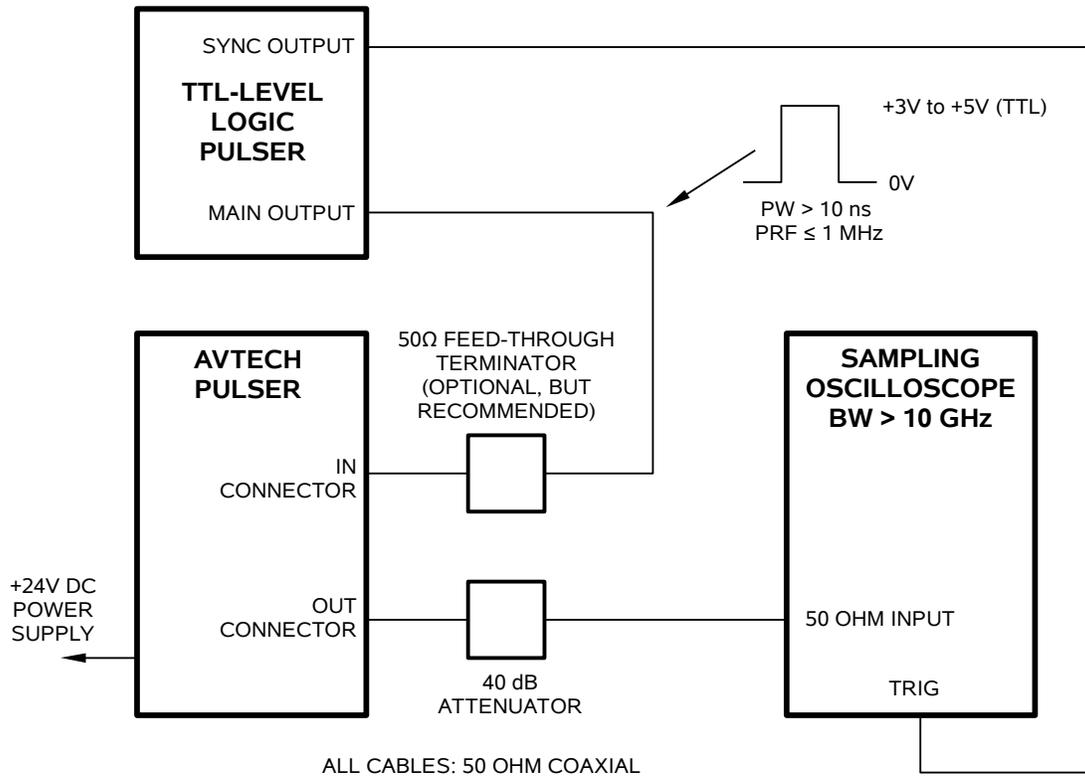
-M Option: a monitor output is provided.

SPECIFICATIONS

Model:	AVMP-2
Amplitude ^{1,2} : (50Ω load)	0 - 10 Volts
Pulse width (FWHM):	5 ns - 100 ns
Maximum PRF:	1 MHz
Maximum duty cycle:	10%
Rise time (20%-80%):	≤ 100 ps
Fall time (80%-20%):	≤ 135 ps
Polarity ³ :	Positive or negative (specify)
Propagation delay:	≤ 30 ns
Jitter:	± 35ps
DC offset:	Apply required DC offset to solder terminals (± 50 Volts, 250 mA max)
Trigger required:	+5 Volts, 10 ns or wider (TTL) ⁵
Monitor output option:	Provides a 20 dB attenuated coincident replica of main output
Connectors:	Trig, Out, Monitor: SMA, Power: Solder terminals
Power requirements:	+24V DC
Dimensions: (H x W x D)	43 mm x 66 mm x 109 mm (1.7" x 2.6" x 4.3")
Chassis material:	Cast aluminum with blue enamel
Temperature range:	+5°C to +40°C

- 1) For analog electronic control (0 to +10V) of amplitude or pulse width with -EA or -EW.
- 2) For operation at amplitudes of less than 20% of full-scale, best results will be obtained by setting the amplitude near full-scale and using external attenuators on the output.
- 3) Indicate desired polarity by suffixing model number with -P or -N.
- 4) Add -M to model number for monitor option.
- 5) Add -ECL to the model number to specify an ECL-level trigger input (-1.6V and -0.8V logic levels) instead of TTL.

BASIC TEST ARRANGEMENT



CONTROLS

CONTROLS - FRONT

The location of the IN and OUT connectors, the optional “M” connector, and the amplitude and pulse width controls are shown in the photo below.



The “AMP” and “PW” controls may be adjusted using a screwdriver. Units with the -EA or -EW options will have solder terminals instead of screwdriver-adjustable controls.

CONTROLS - REAR

The +24V DC power input and the ground terminal are located on the rear of the module.

An “OS” offset input solder terminal is present on the rear panel as well.

GENERAL OPERATING NOTES

- 1) The bandwidth capability of components and instruments used to display the pulse generator output signal (attenuators, cables, connectors, etc.) should exceed 10 GHz.
- 2) The use of 40 dB attenuator on the output will ensure a peak input signal to the sampling scope of less than 1 Volt.
- 3) In general, the source pulse generator trigger delay control should be set in the 0.1 to 1.0 us range, for proper positioning of the output pulse on the sampling oscilloscope display.
- 4) When testing using a general purpose 50 ohm laboratory pulse generator as the input trigger signal source, the input signal should be applied via a 50 ohm feed-through terminator, or alternatively the input to the AVMP-2 unit should be shunted with a 50 ohm resistor. This will prevent reflection (and degradation of the input pulse waveform) caused by the high impedance at the IN port. When triggering from a TTL source, no 50 ohm feed-through load or resistor is necessary but lead length should be as short as possible. High-speed TTL Schottky logic is recommended for the driving circuitry.
- 5) The input trigger pulse width should be greater than 10 ns and less than one half of the pulse repetition frequency period. The unit triggers on the leading edge of the input trigger signal.
- 6) The output pulse width is controlled by means of the one-turn potentiometer (PW), except on -EW units. The pot should initially be set maximum clockwise and the pulse width adjusted using an oscilloscope. On -EW units, apply 0 to +10V to the PW solder terminal to control the pulse width. Note that the output pulse may vanish entirely when the control voltage is zero.
- 7) The output pulse amplitude is controlled by means of the one-turn potentiometer (AMP), except on -EA units. On -EA units, apply 0 to +10V to the AMP solder terminal to control the amplitude.
- 8) WARNING: The module may fail if triggered at a PRF greater than 1 MHz.
- 9) The monitor output port (M) provides a coincident attenuated ($\div 10$) replica of the main output to a 50 ohm load. (Option).

PERFORMANCE CHECK SHEET