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BOX 5120, LCD MERIVALE OTTAWA, ONTARIO CANADA K2C 3H5

## PERFORMANCE CHECKSHEET

Model: AVO-9B2-B-P-P1B-T1B-AK1-AK8-VXI-R5
Type: Ultra-High-Speed Laser Diode Driver
S.N.: 13723

Date: May 17, 2018
Basic specifications: $\rightarrow$

## Test Waveforms

Mainframe output into 50 Ohm load at 100 kHz, $600 \mathrm{ps},+23 \mathrm{~V}$,
$500 \mathrm{ps} /$ div. $10 \mathrm{~V} / \mathrm{div}(100 \mathrm{mV} / \mathrm{div} \times 40 \mathrm{~dB}$ ):


Output Amplitude:
up to +23 V , to $50 \Omega$
Pulse Width (FWHM): 0.6 ns -1 us
Rise Time (20\%-80\%): $\leq 200$ ps
Fall Time ( $80 \%-20 \%$ ): $\leq 200 / 500$ ps
PRF: $\quad 1 \mathrm{~Hz}-100 \mathrm{kHz}$
Jitter, Stability: OK
Prime Power: $\quad 100-240 V \mathrm{AC}, 50-60 \mathrm{~Hz}$.

Mainframe output into 50 Ohm load at 100 kHz , 4 ns, +23V,

1 ns/div. $10 \mathrm{~V} / \mathrm{div}(100 \mathrm{mV} / \mathrm{div} \times 40 \mathrm{~dB}$ ):


Mainframe output into 50 Ohm load at 100 kHz, 30 ns, +23V,
$5 \mathrm{~ns} /$ div. $10 \mathrm{~V} / \mathrm{div}(100 \mathrm{mV} / \mathrm{div} \times 40 \mathrm{~dB}$ ):


Mainframe output into 50 Ohm load at 10 kHz , 1 us, +23V,
$200 \mathrm{~ns} /$ div. $10 \mathrm{~V} / \mathrm{div}(100 \mathrm{mV} / \mathrm{div} \times 40 \mathrm{~dB}$ ):



Top waveform: Voltage across the parallel combination of the $4.5 \Omega$ effective resistance. It should be approximately $(+23 \mathrm{~V} / 54.5 \Omega) \times 4.5 \Omega=$ +1.9 V in amplitude, which agrees approximately with the observed waveform.

Bottom waveform: "MI" output, approximately $+23 \mathrm{~V} / 11$.

Both: 2 ns/div, 1 V/div (100 mV/div $\times 20 \mathrm{~dB}$ ).

Test method: Short leads are soldered across a chip resistor. A coaxial cable is soldered across the resistor. The signal lead is inserted into the anode pin socket. The ground lead is inserted into one of the other pin sockets (which are grounded). The total effective resistor is $5 \Omega \|$ $50 \Omega\left(\mathrm{R}_{\mathrm{scope}}\right)=4.5 \Omega$.


