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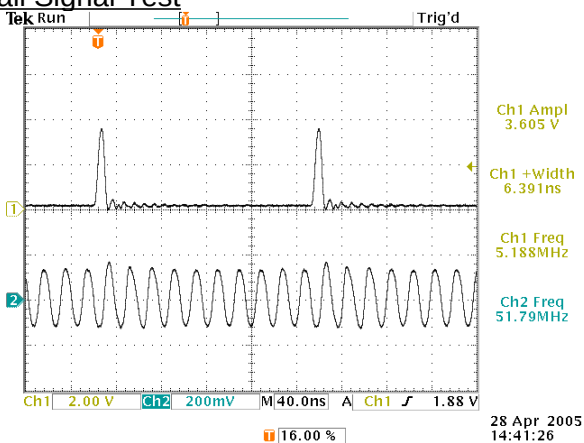
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PERFORMANCE CHECKSHEET

Model: AVX-FD2-PS-EP-IP-XN
S.N.: 11188
Date: March 22, 2005

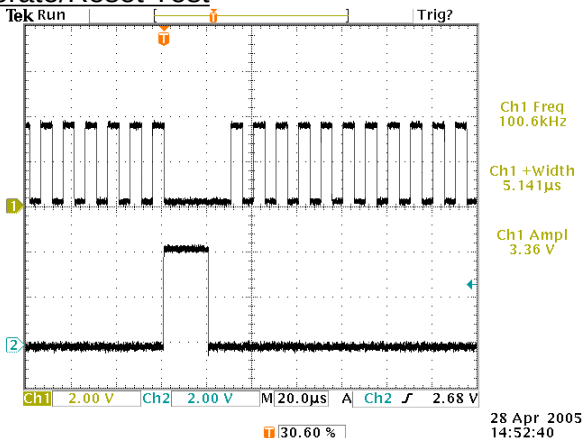
Small Signal Test



Top: OUT, ÷ 10, 50Ω load. 2 V/div.
Bottom: 50 MHz sine wave in. 200 mV/div.
Both: IP 50Ω mode, 40 ns/div.

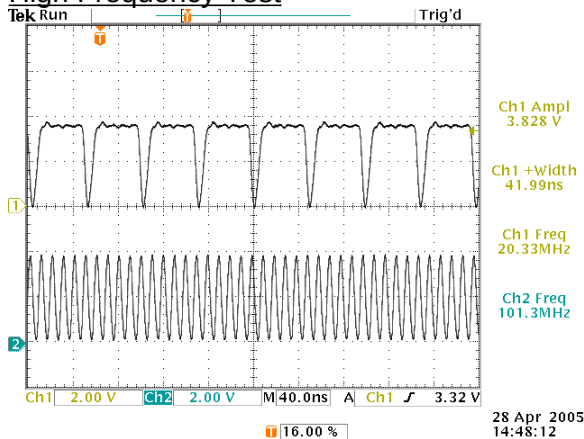
- a) Output Signal Amplitude: TTL, $R_L \geq 50\Omega$
- b) Pulse Width: 5 ns - 5 us
- c) Division Factor: 0 to 65535
- d) PRF: 0 - 100 MHz
- e) Jitter, Stability: OK
- f) Prime Power: 100-240V AC, 50-60 Hz.

Operate/Reset Test



Top: OUT, ÷ 1000, $f_{IN} = 100$ MHz, 2 V/div.
Bottom: RESET input. 2V/div.
Both: TTL 50Ω mode, 20 us/div.

High Frequency Test



Top: NOT-OUT, ÷ 5, 50Ω load. 2 V/div
Bottom: 100 MHz TTL in. 2 V/div.
Both: TTL 50Ω mode, 40 ns/div.