

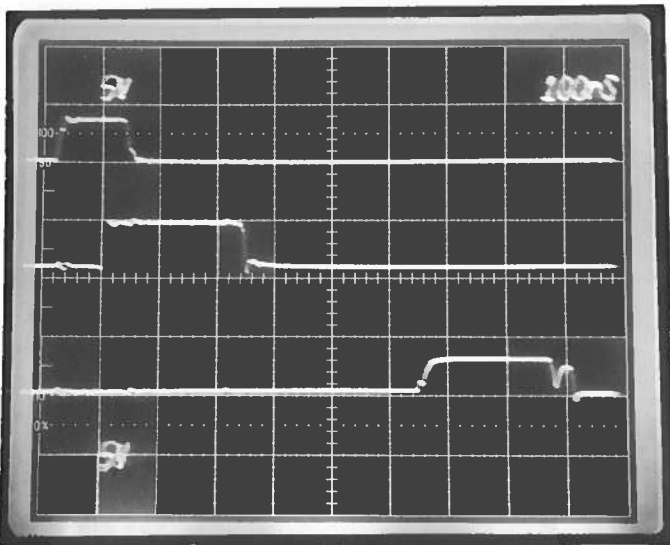
PULSE GENERATOR

PERFORMANCE CHECK

Model: *MX-D-PS-EO-IBM2*

S.N.: *7975*

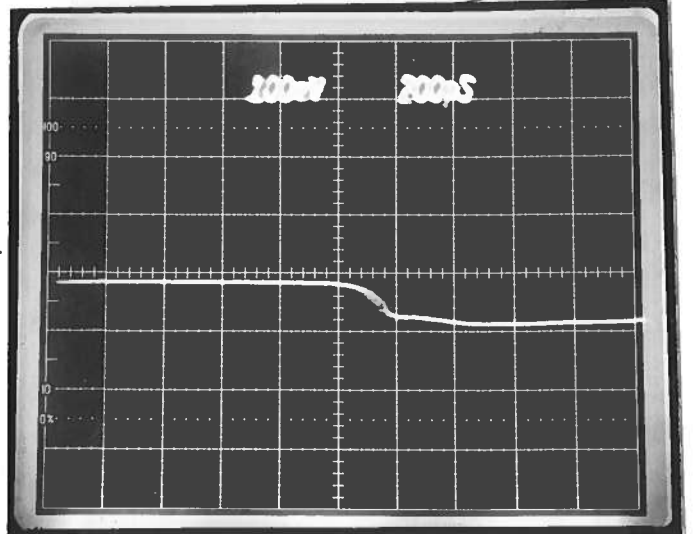
Date: *APRIL 29 1997.*



- a) Output signal amplitude: *TTL (R_L ≥ 50 Ω)*
- b) Pulse widthout: *250 NS*
- c) DELAY: *30 NS TO 1.0 μS.*
- d)
- e) PRF: *0 TO 1.0 MHz*
- f) Jitter, stability: *OK*
- g) Prime power: *120/240 V*
50-60 Hz

①
TOP TRIG IN
MID CHANNEL 1 OUT 40 1K
DELAY ≈ 70 NS.
BOT CHANNEL 2 OUT TO 50 Ω
DELAY ≈ 620 NS.
PRF ≈ 100 kHz

SITTER
CUTERK
MX-D TRIG IN WITH
VAK-N WITH DELAY
≈ 100 NS. VAK-N HAS
100 NS RISE TIME.



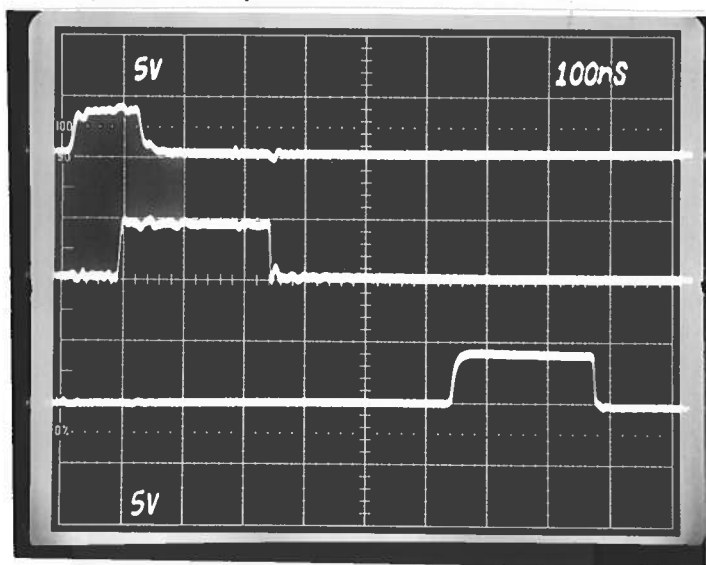
②

PULSE GENERATOR
PERFORMANCE CHECK

Model: APR-D 75-60-IBM 2

S.N.: 7975 (M.D)

Date: JUNE 17 1997



- a) Output signal amplitude:
TTL ($R_L \approx 50\Omega$)
- b) Pulse width OUT:
250 NS
- c) DELAY:
30 NS TO 1.0 US
- d) -
- e) PRF: 0 TO 100 kHz

TDR TRIG IN

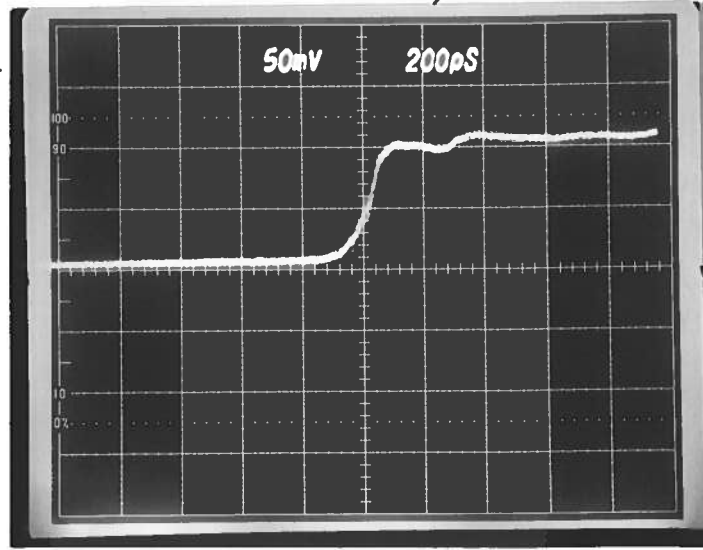
MID CHANNEL 1 OUT TO 1K
DELAY ≈ 80 NS

BOI CHANNEL 2 OUT TO 50 Ω
DELAY ≈ 630 NS
PRF ≈ 100 kHz

- f) Jitter, stability: OK
- g) Prime power: 120/240V ^{50.6014}

JITTER CHECK

APR-D TRIG/600MHz
MID 27-PS
WITH DELAY ≈ 100 NS



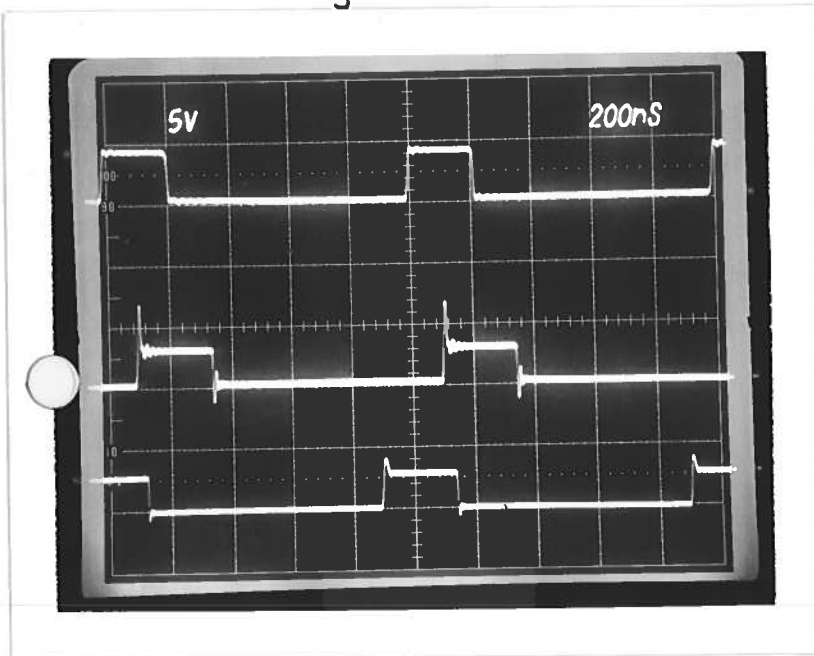
③

PULSE GENERATOR
PERFORMANCE CHECK

Model: AVX-D-PS-ED-IBM2

S.N.: 7975 (MOD 1)

Date: Aug. 10, 1998



- a) Output signal amplitude: TTL ($R_L \geq 50\Omega$)
- b) Pulse width: 250ns
- c) Delay: 30ns to 1.0µs
- d) Drift: x10 lower than before
- e) PRF: 0 to 1.0MHz
- f) Jitter, stability: OK
- g) Prime power: 120/240V
50-60Hz

Top: Trigger In
PRF = 1MHz

Middle: Channel 1 out to 1kΩ
Delay ≈ 100ns

Bottom: Channel 2 out to 50Ω
Delay ≈ 900ns

Jitter Check →
AVX-D-PS-ED-IBM2
triggering AVMP-2-PS.
Delay ≈ 100ns (1MHz)

